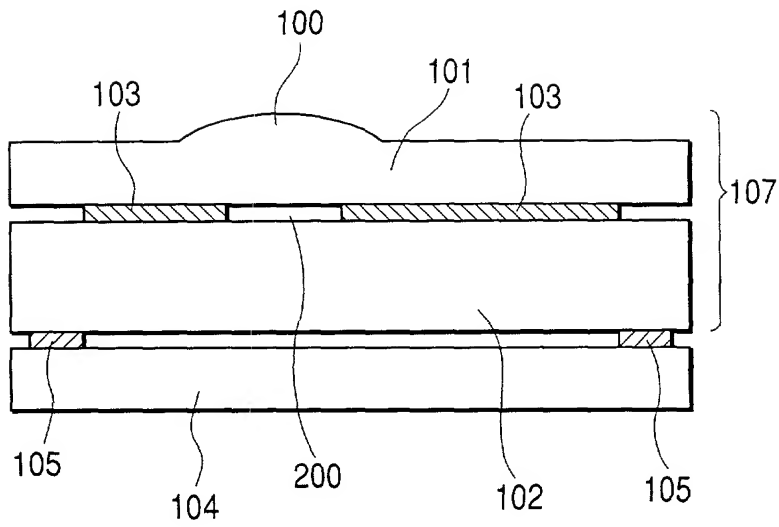


**FIG. 1A**



**FIG. 1B**

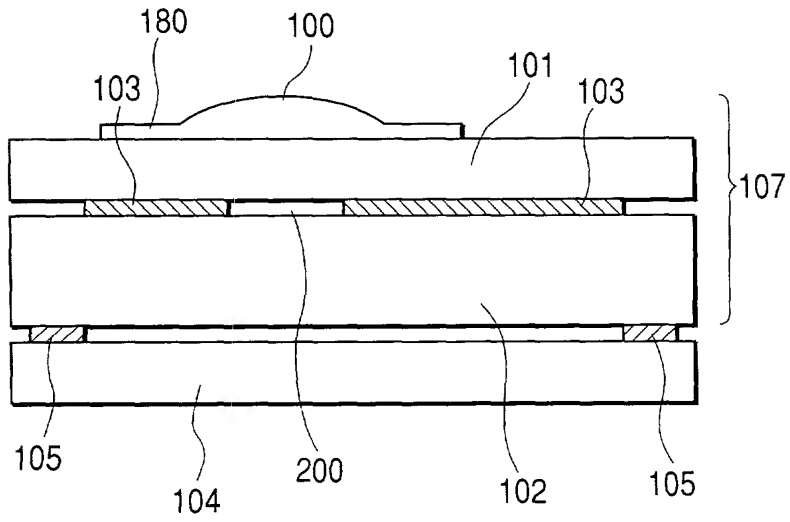


FIG. 2A

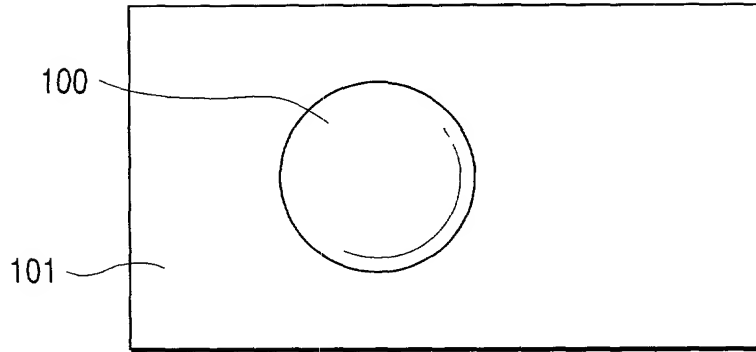


FIG. 2B

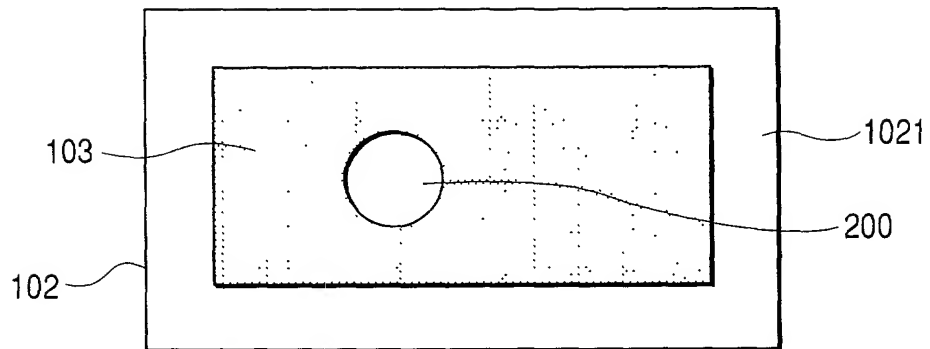
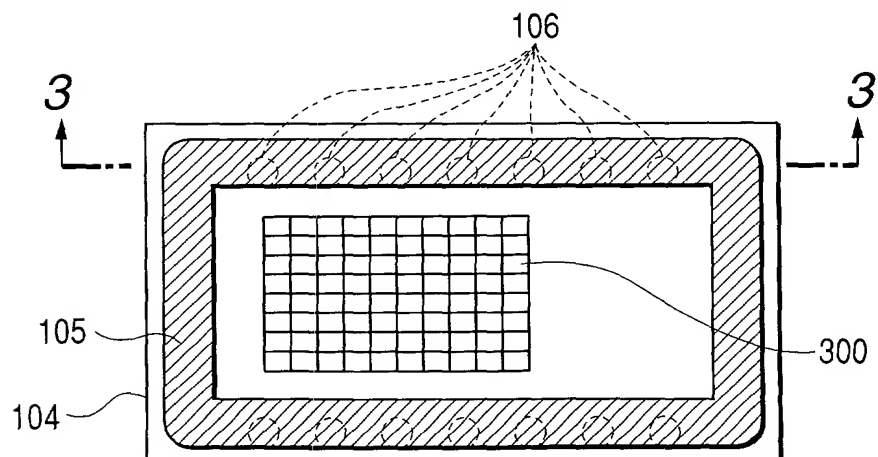
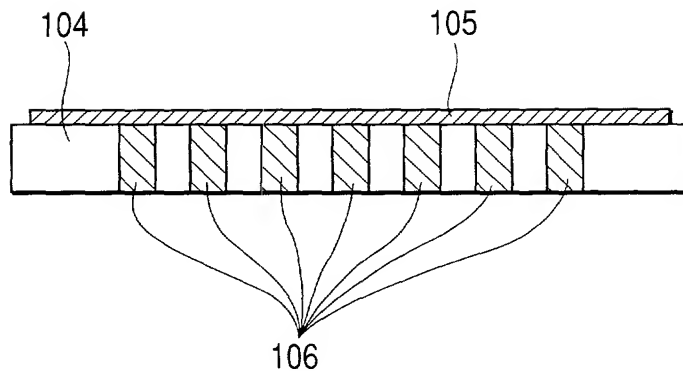


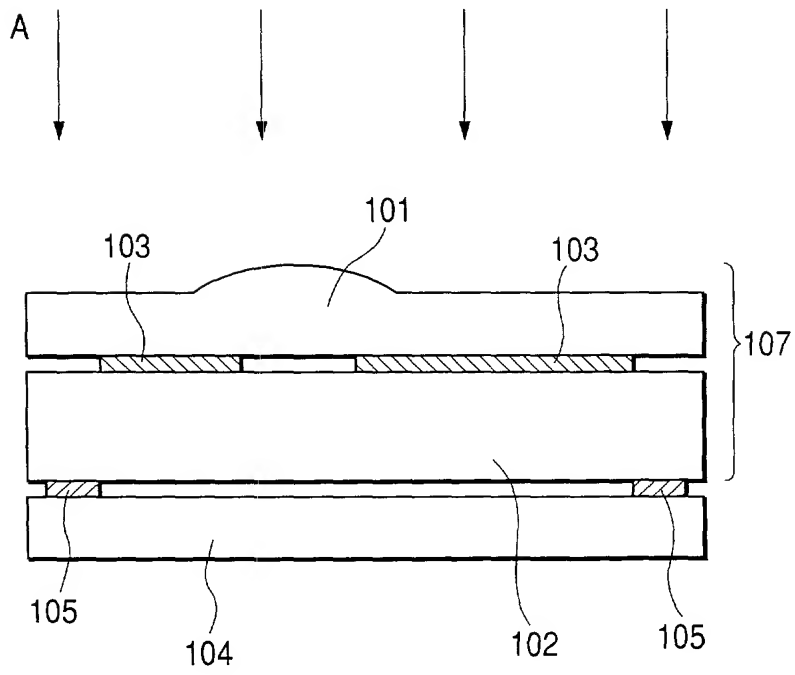
FIG. 2C



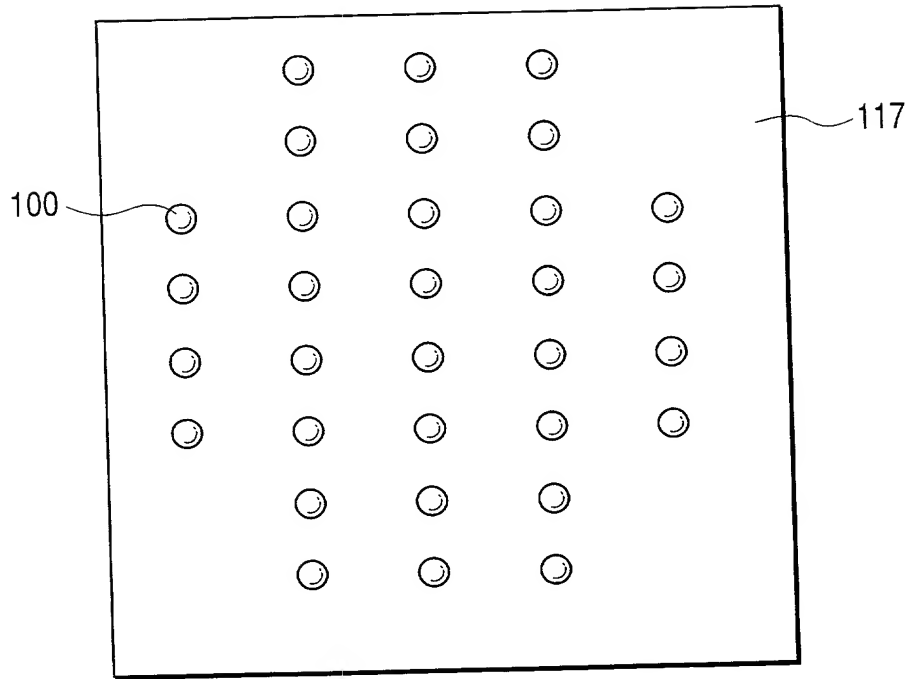
**FIG. 3**



**FIG. 4**



**FIG. 5**



**FIG. 6**

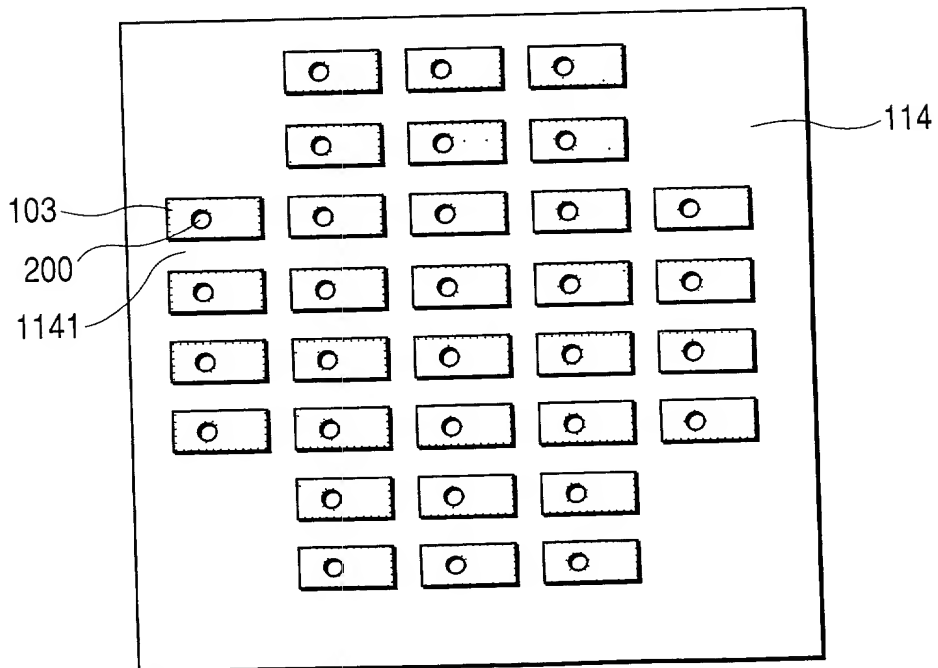


FIG. 7

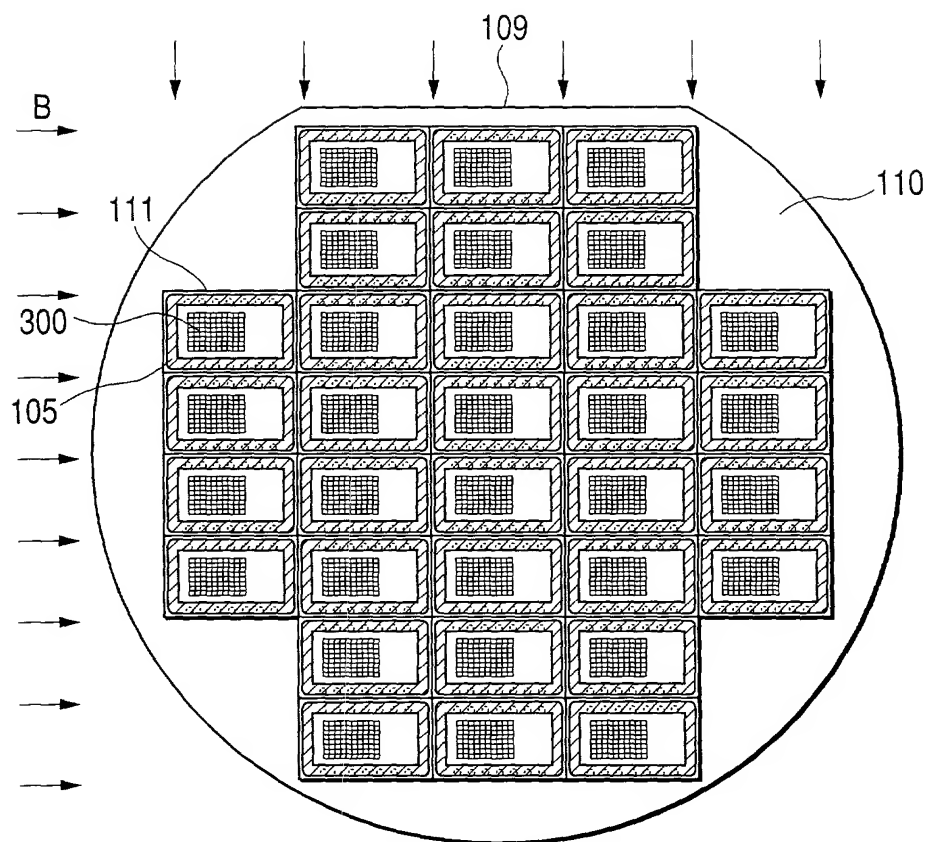


FIG. 8

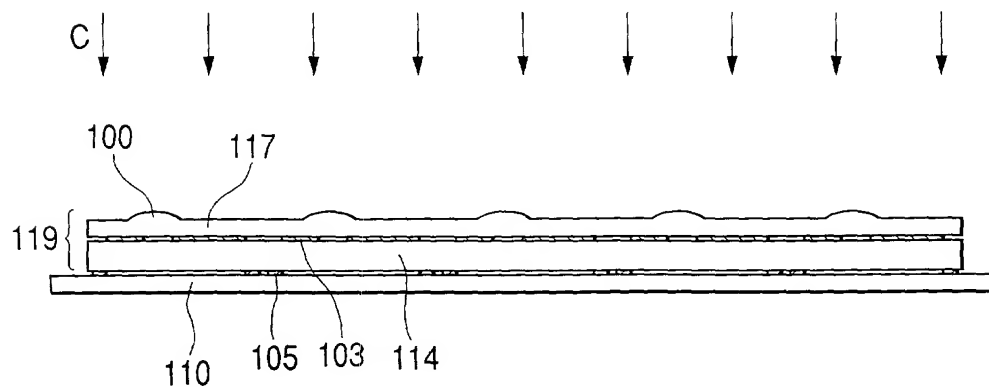


FIG. 9A

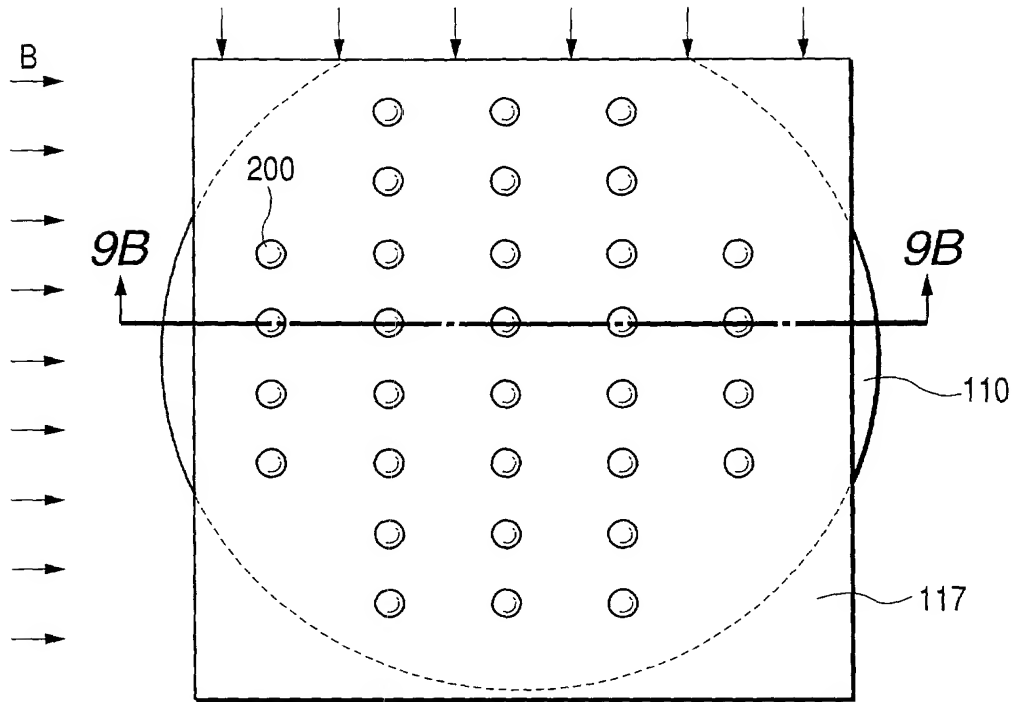
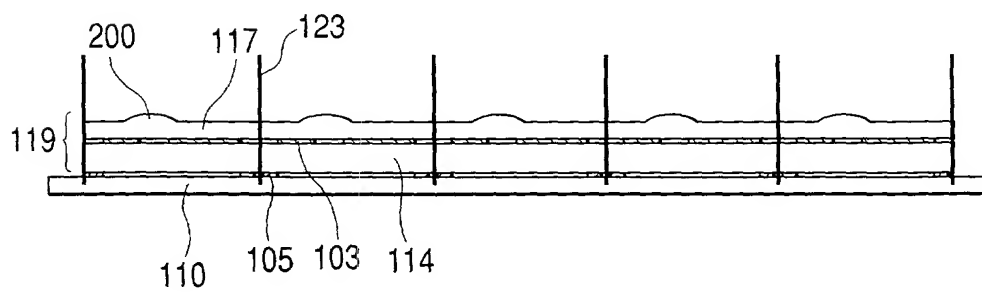
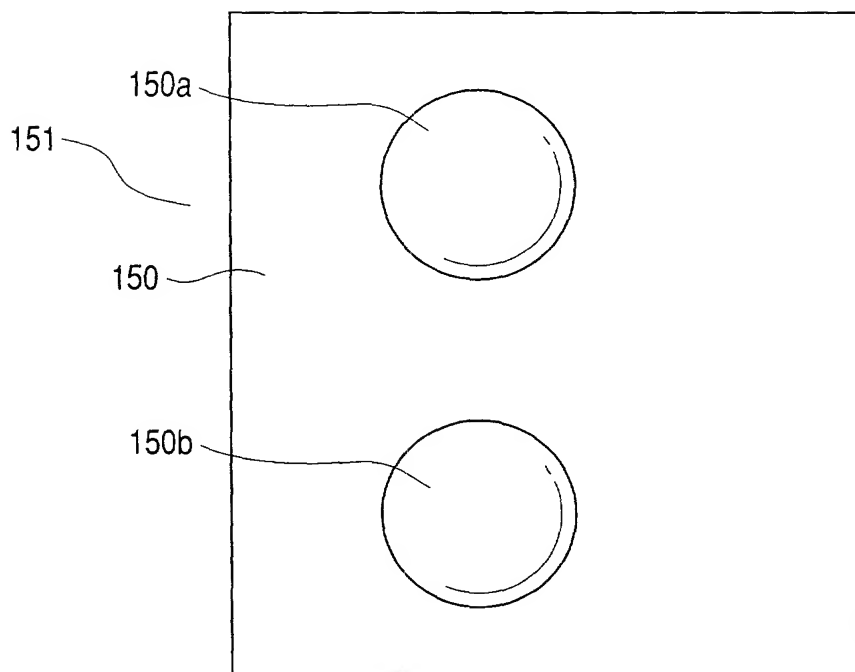


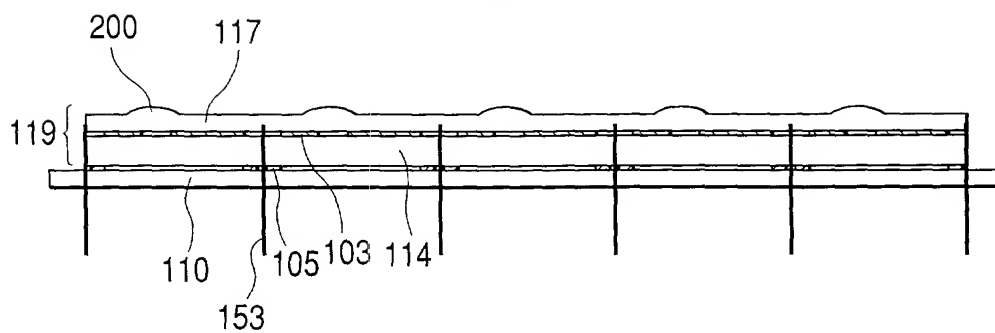
FIG. 9B



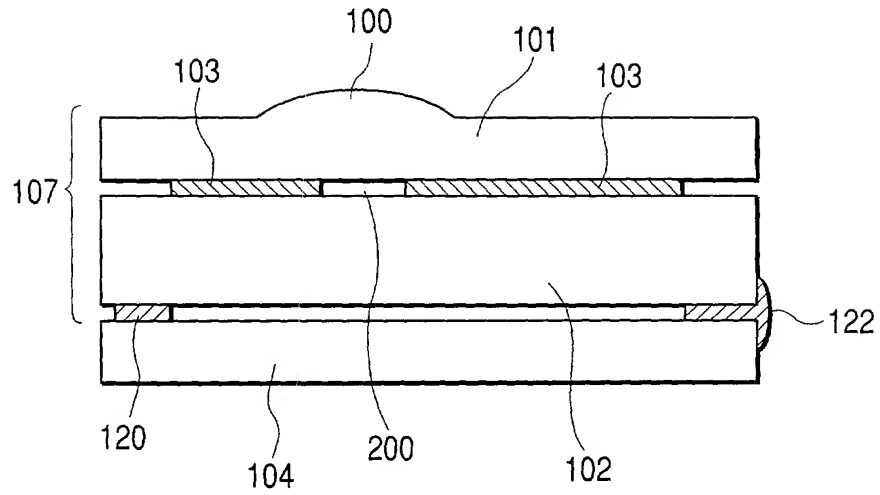
**FIG. 10**



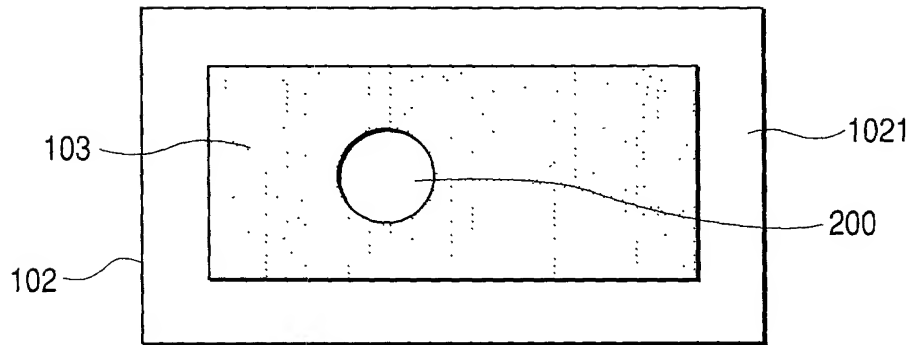
**FIG. 11**



**FIG. 12**



**FIG. 13A**



**FIG. 13B**

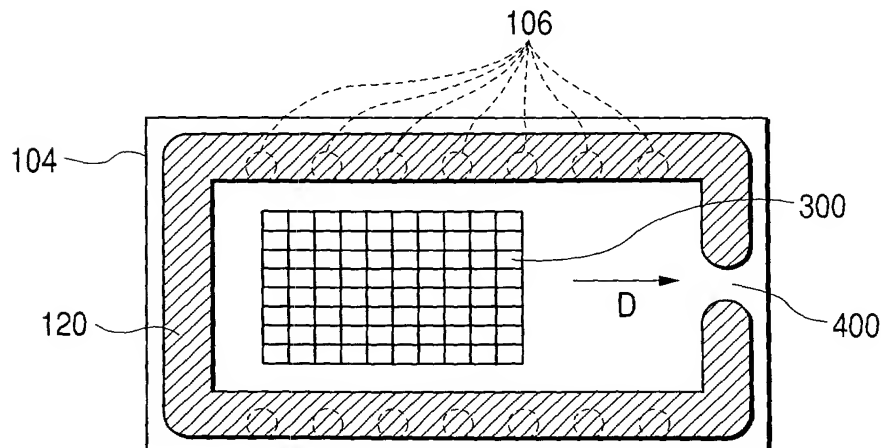




FIG. 14

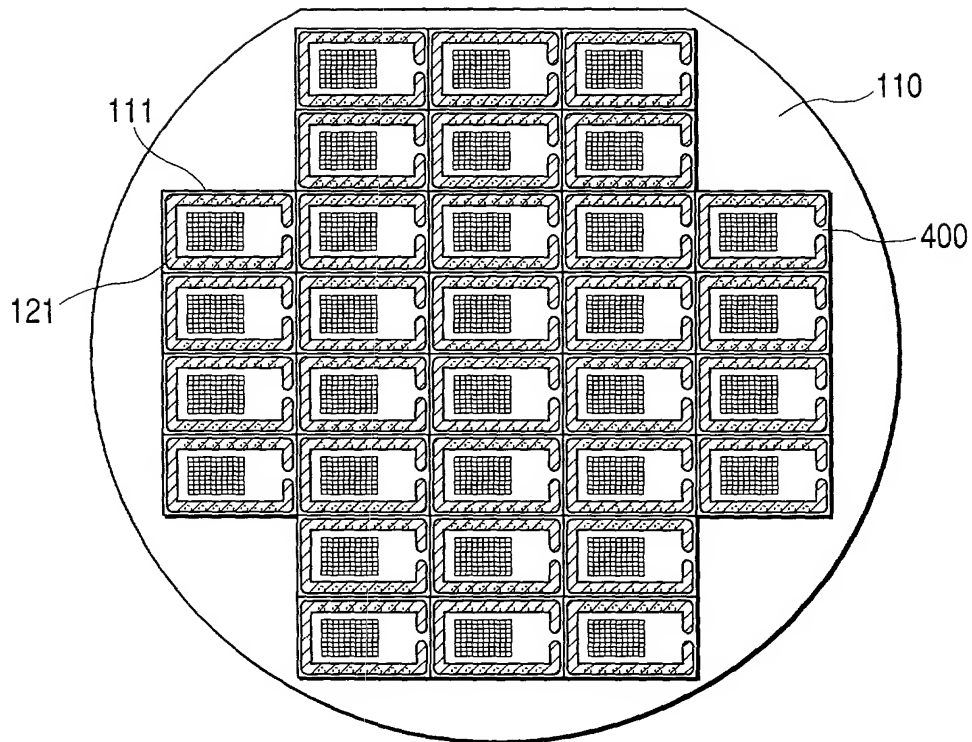


FIG. 15A

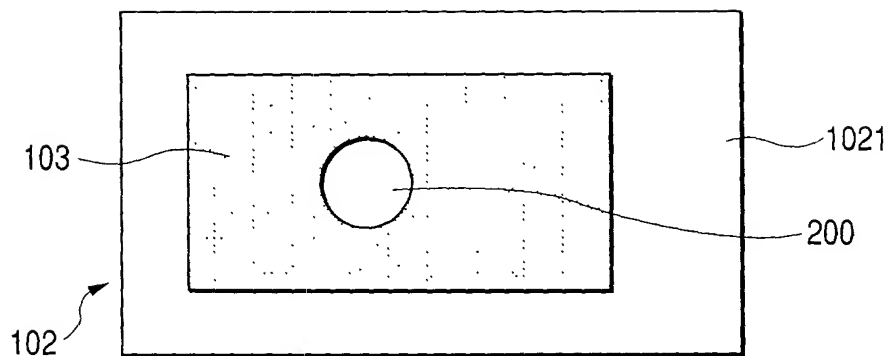


FIG. 15B

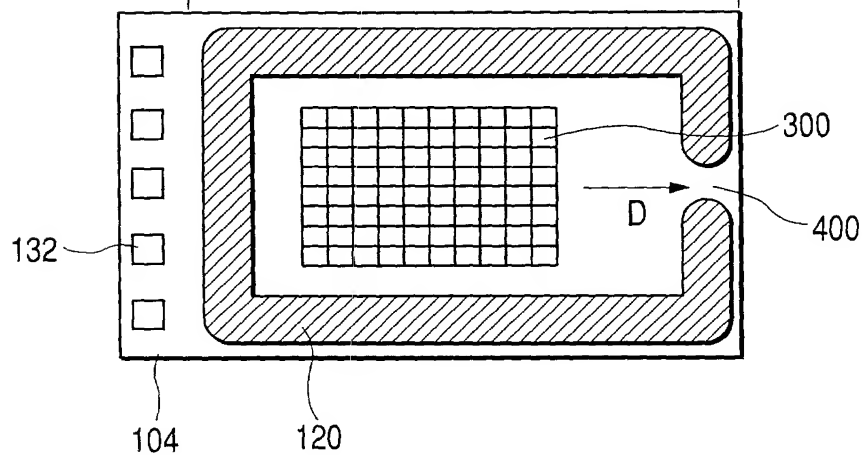


FIG. 16

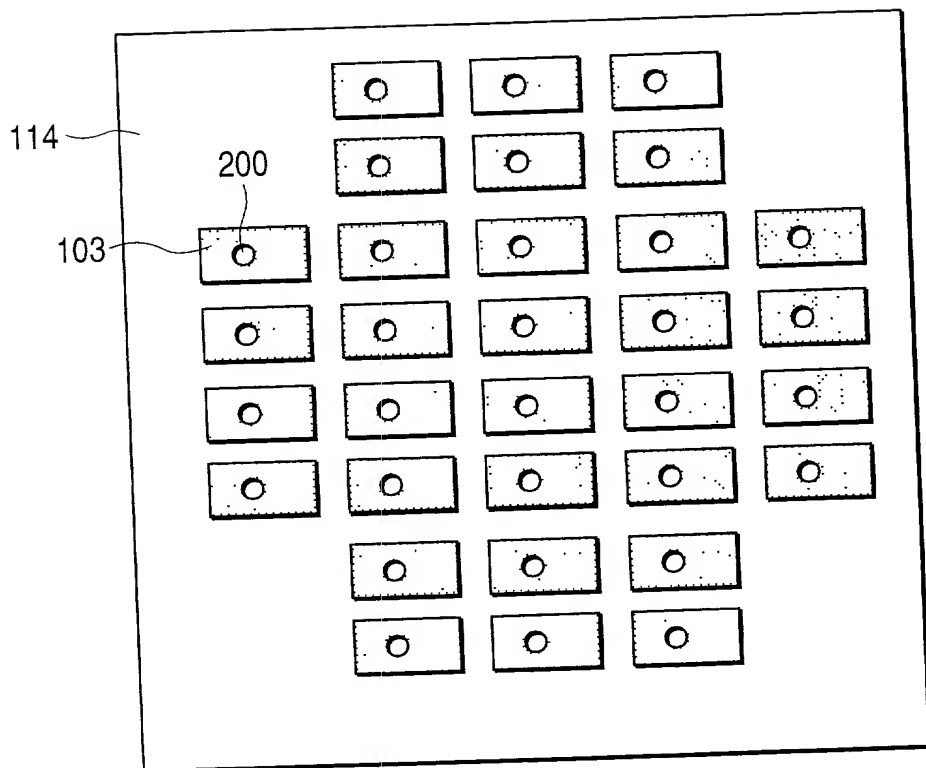
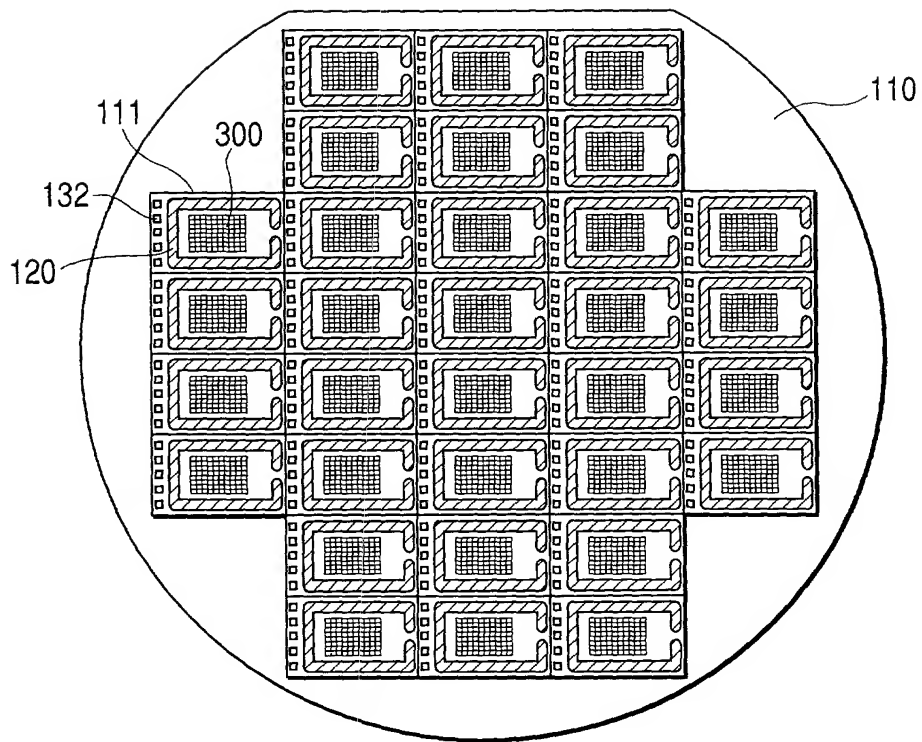
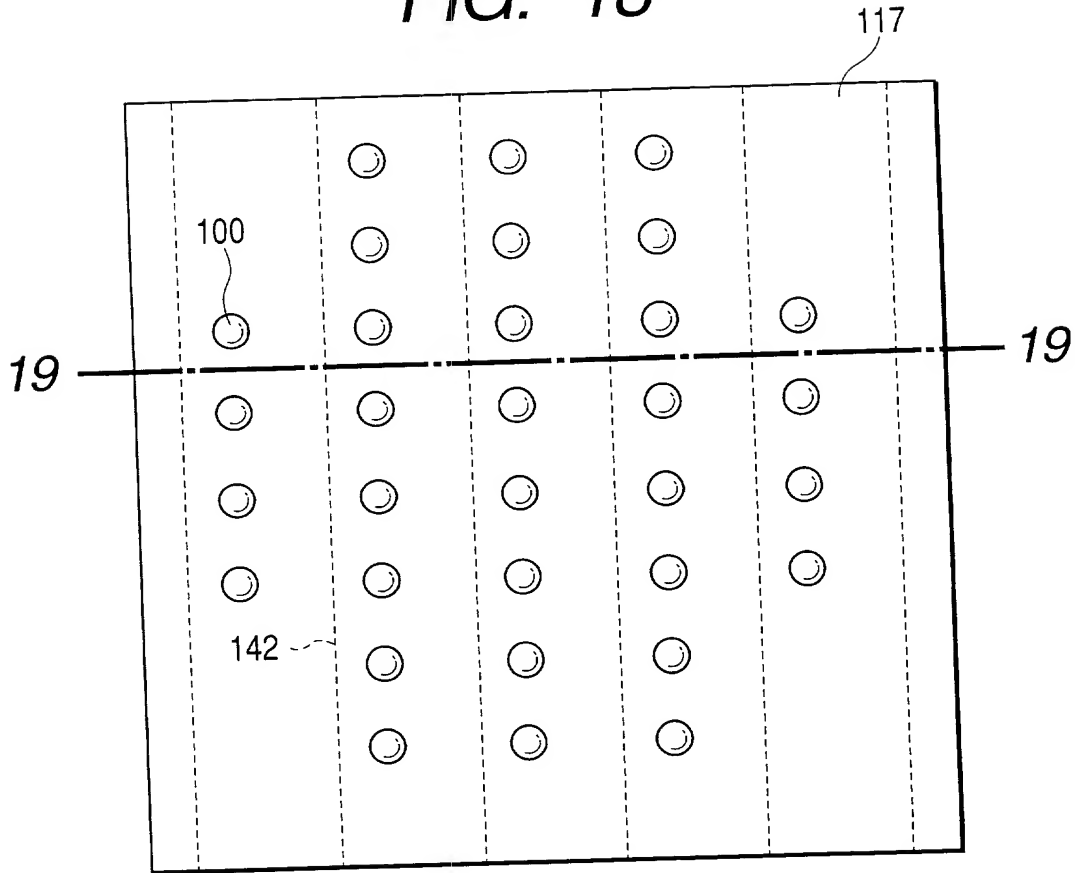


FIG. 17



**FIG. 18**



**FIG. 19**

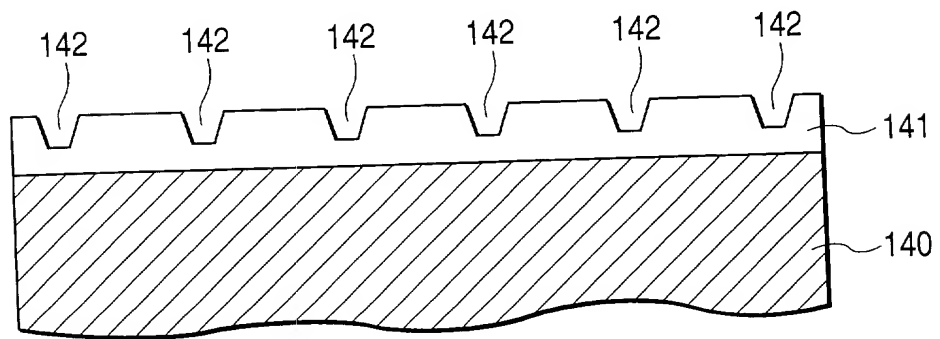


FIG. 20

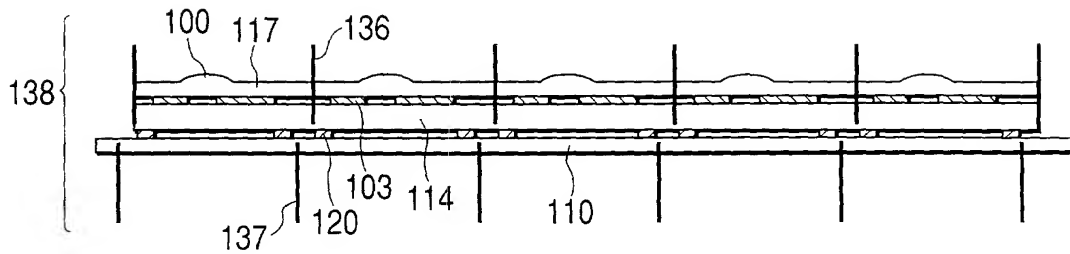


FIG. 21

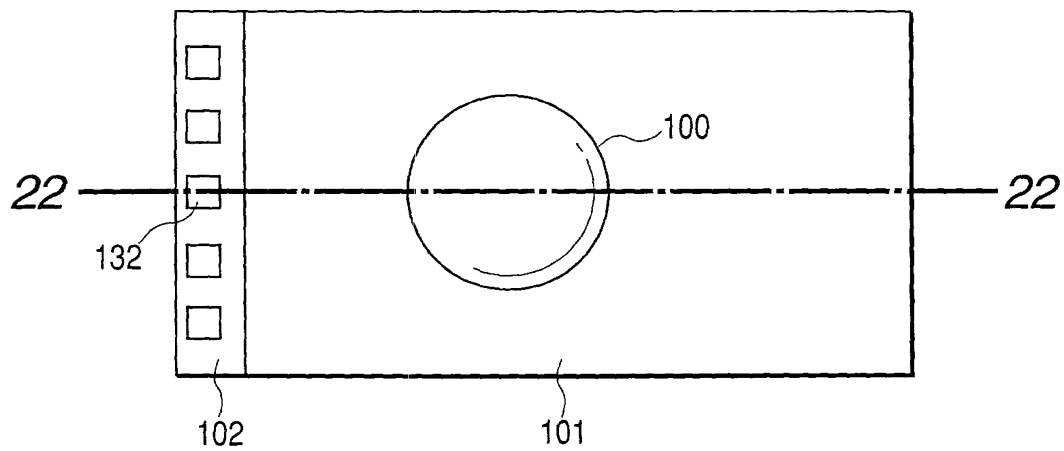
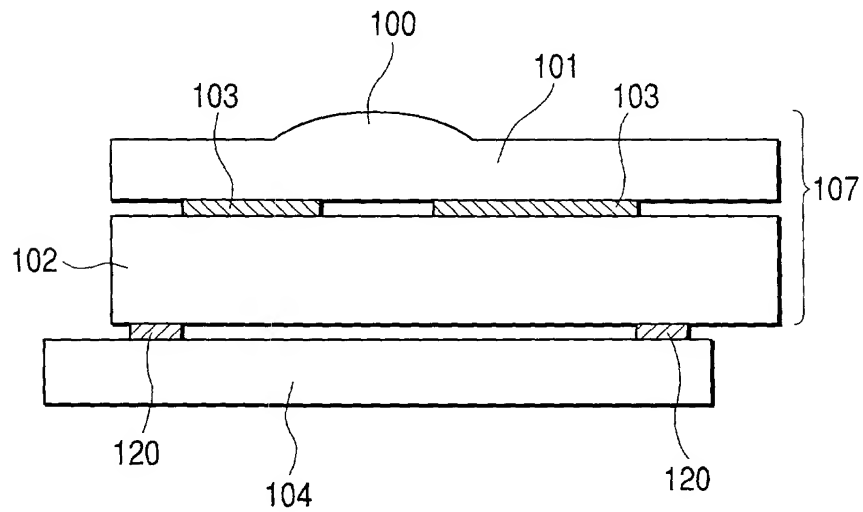
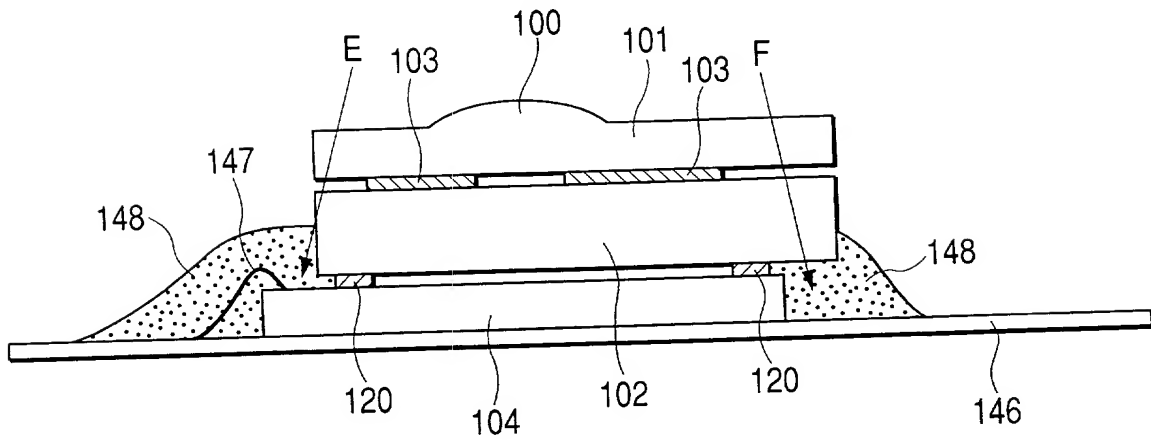


FIG. 22



**FIG. 23**



**FIG. 24**

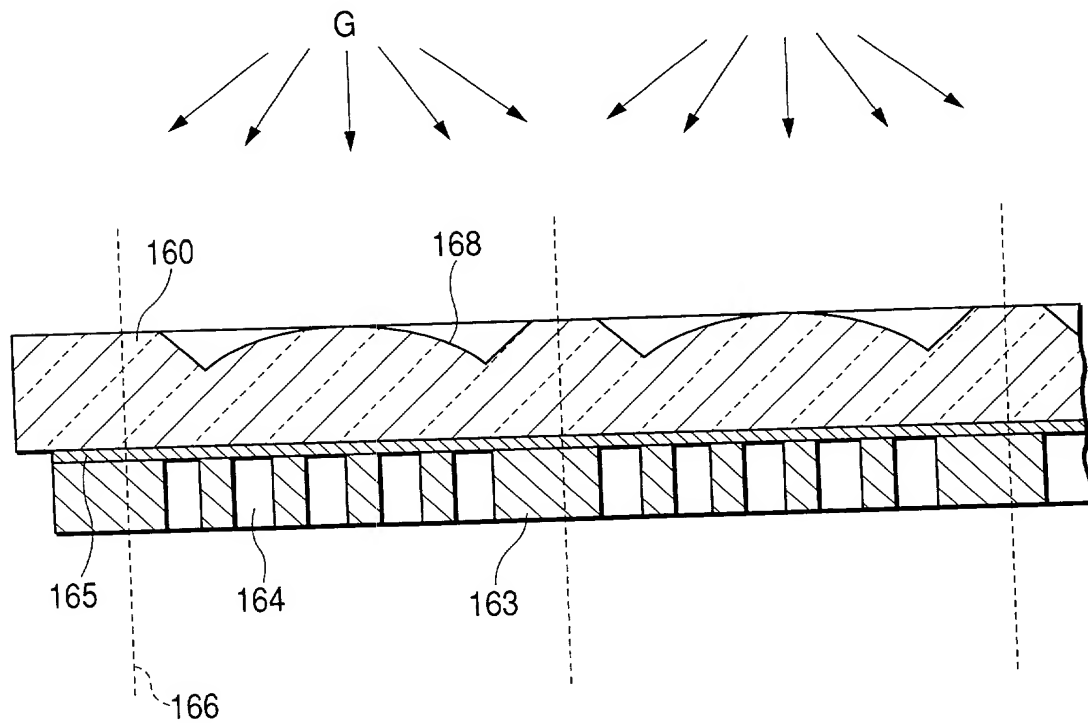


FIG. 25

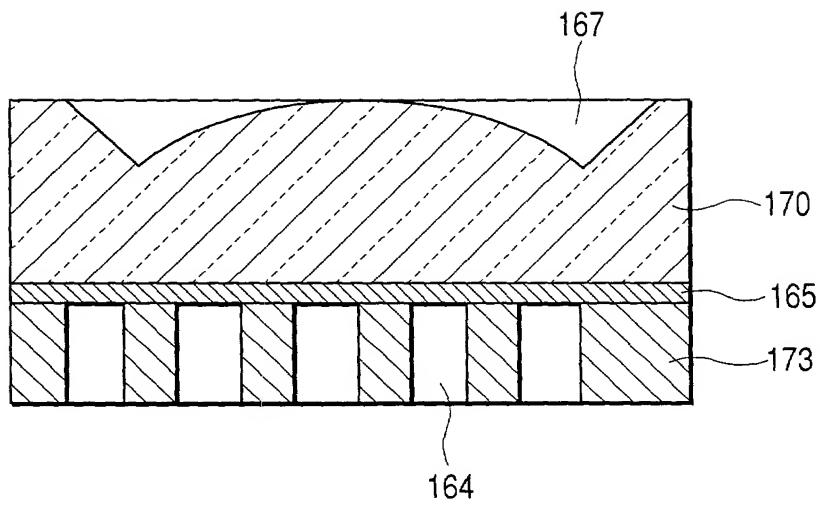




FIG. 26A

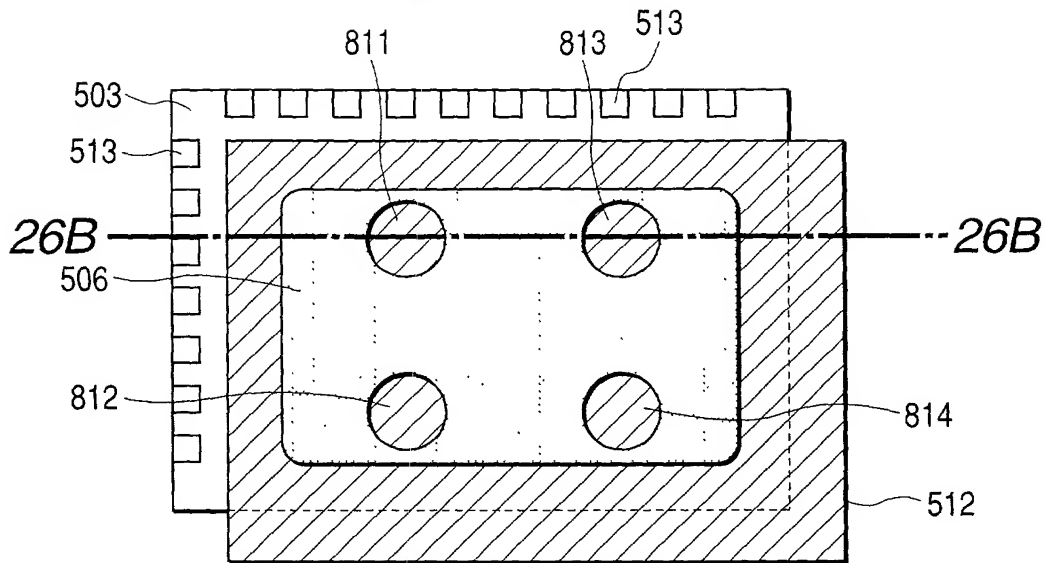


FIG. 26B

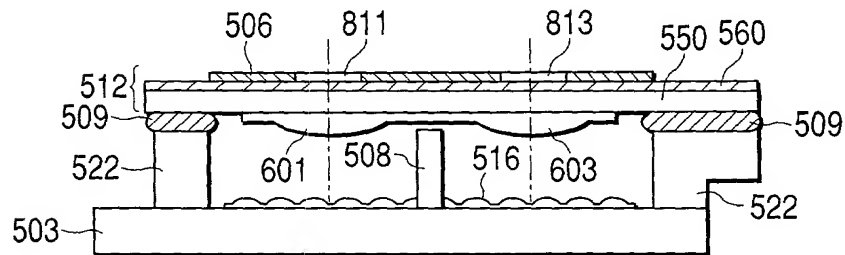


FIG. 26C

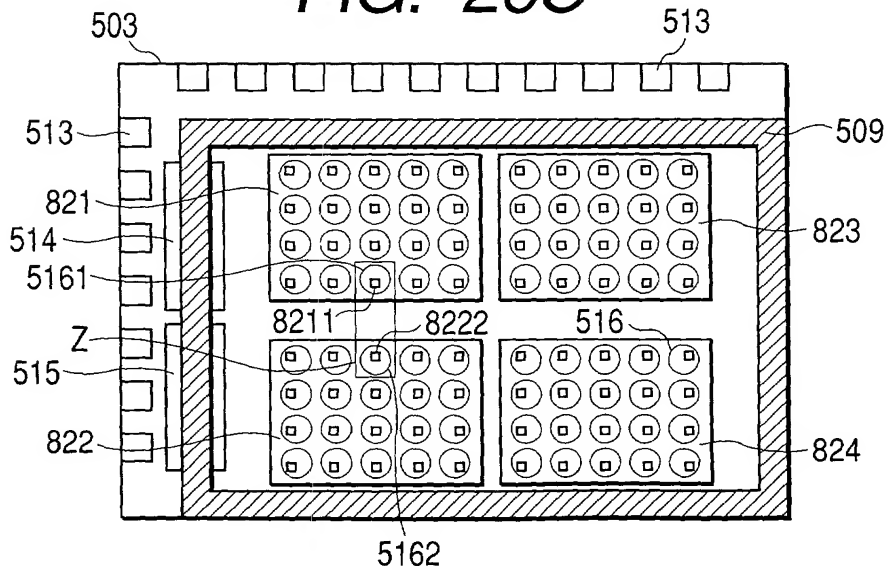


FIG. 27

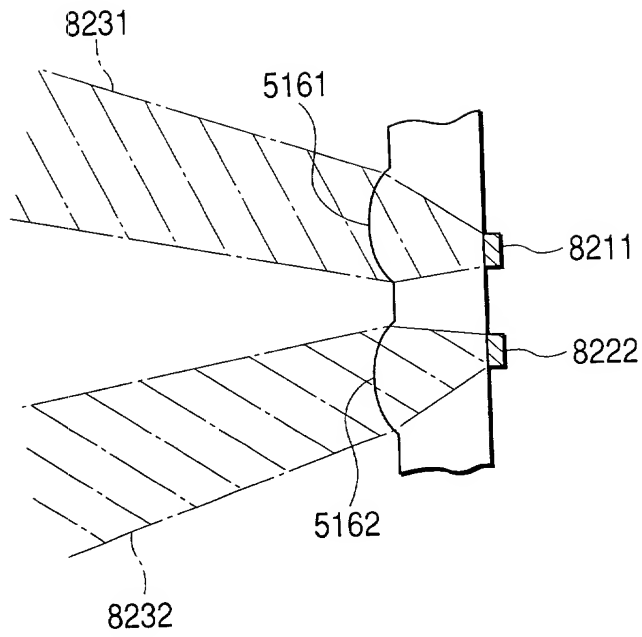


FIG. 28

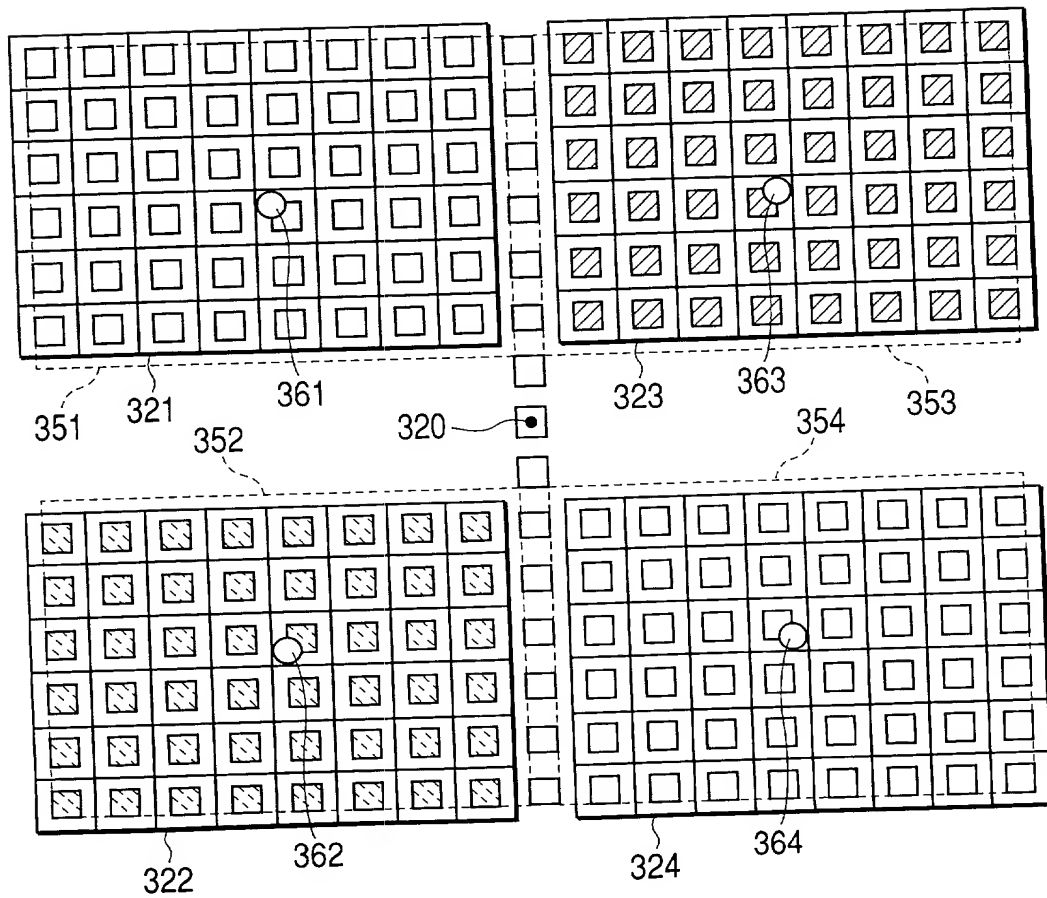
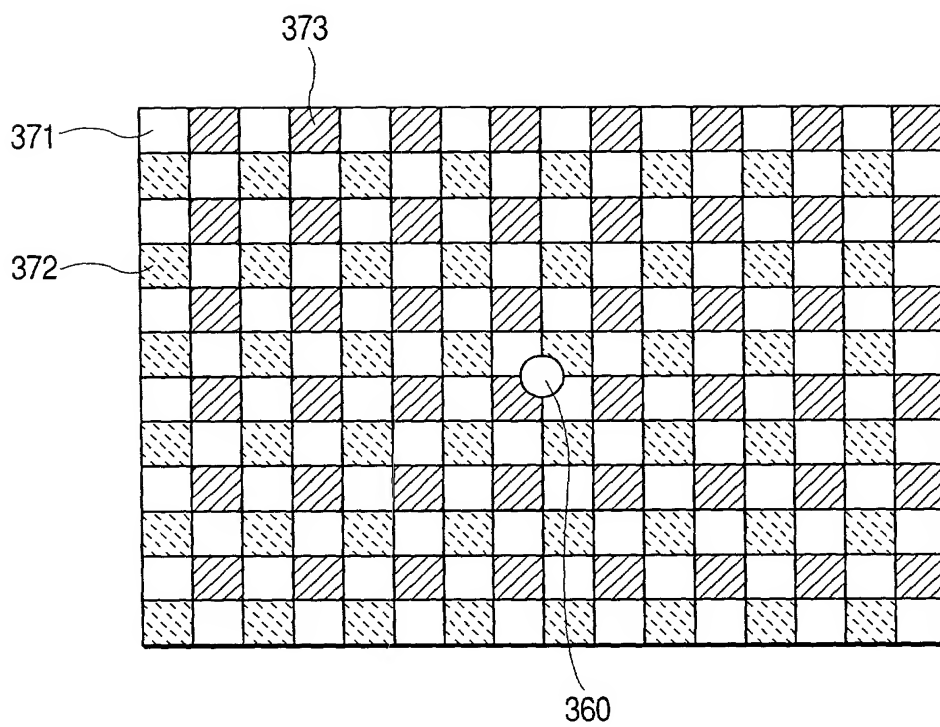
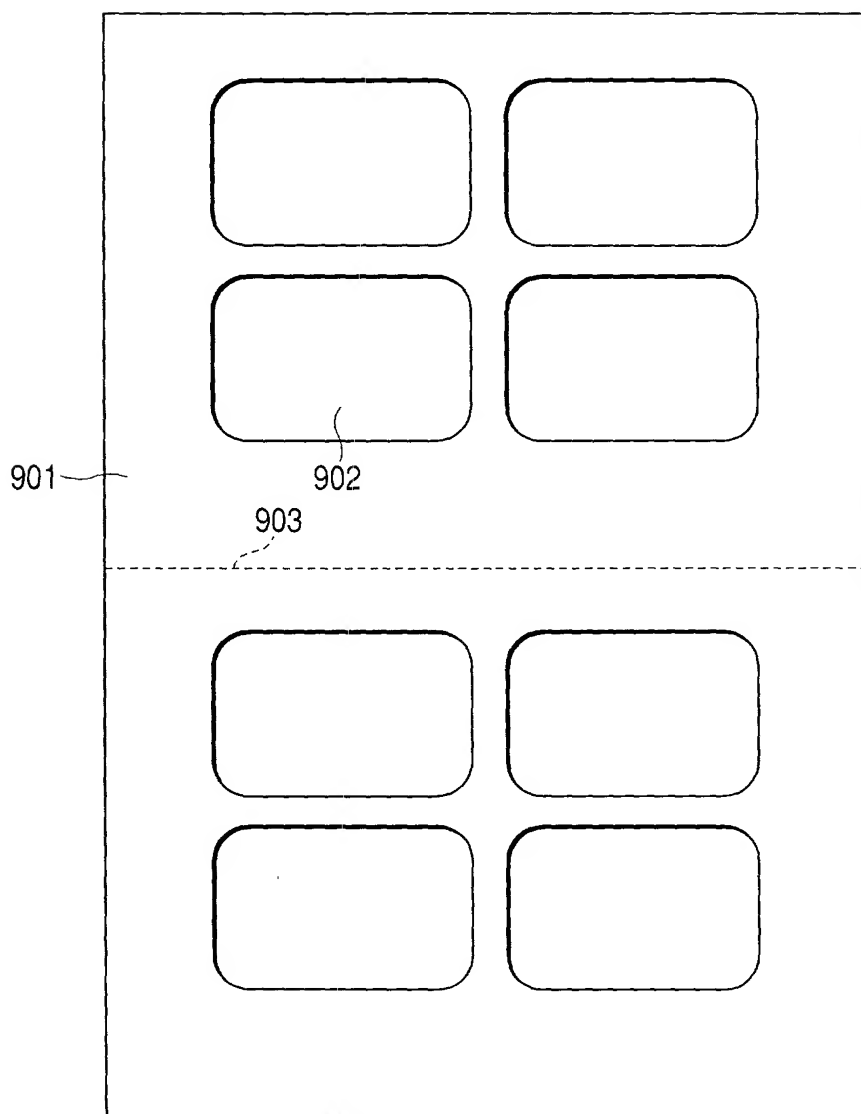


FIG. 29



*FIG. 30*



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FIG. 31

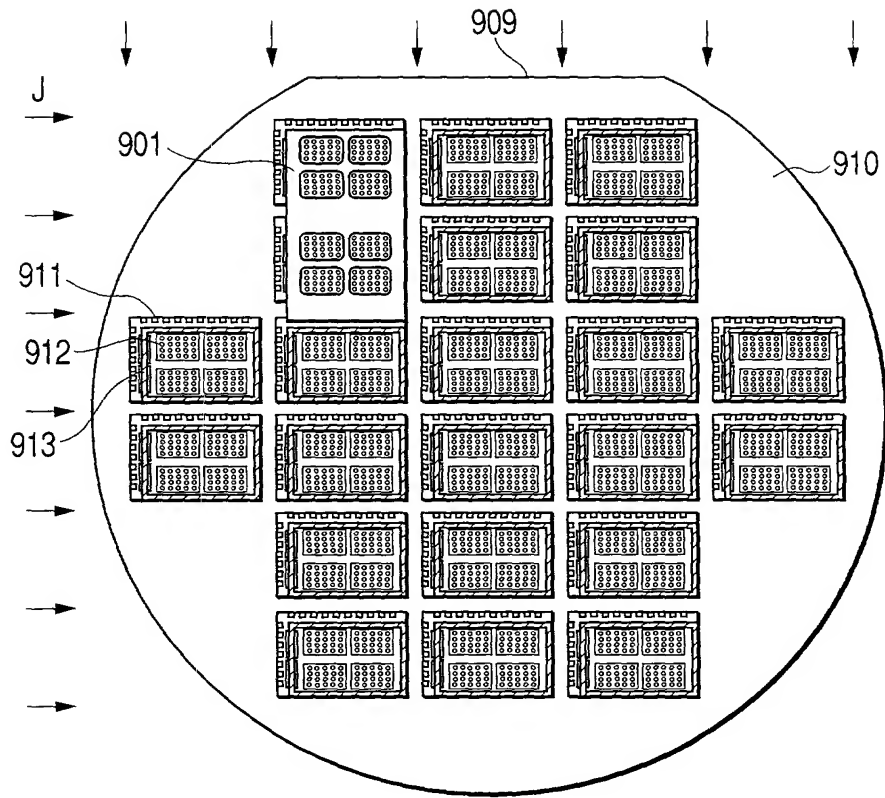
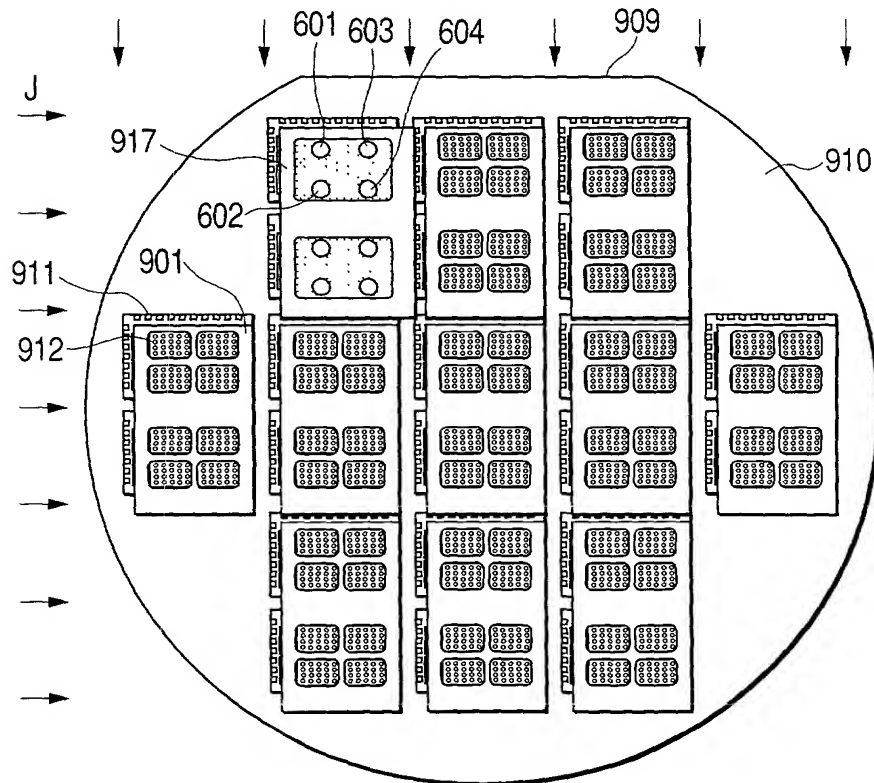


FIG. 32



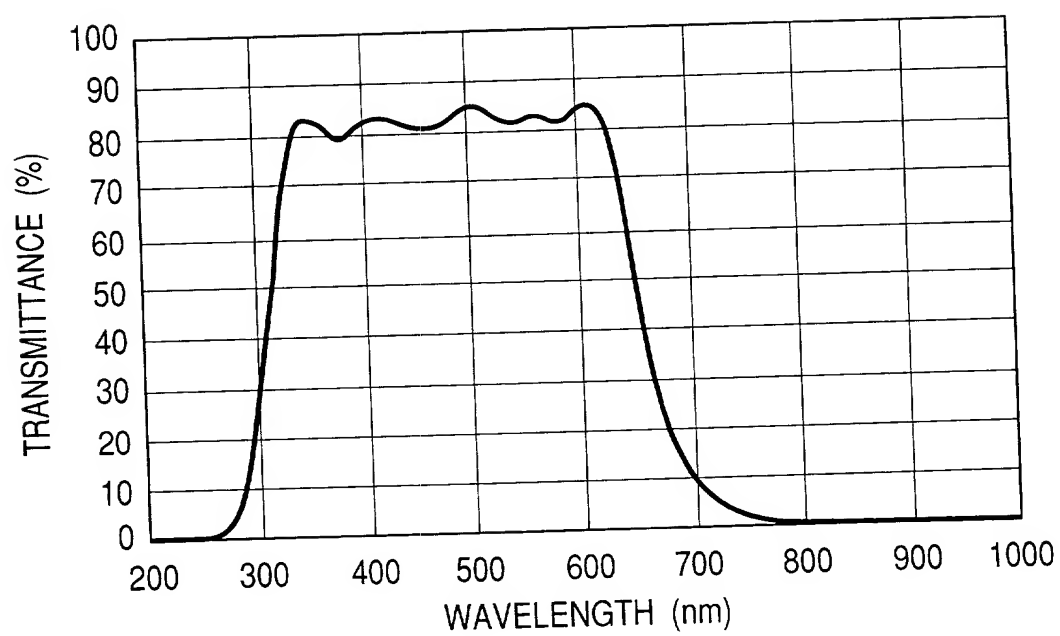
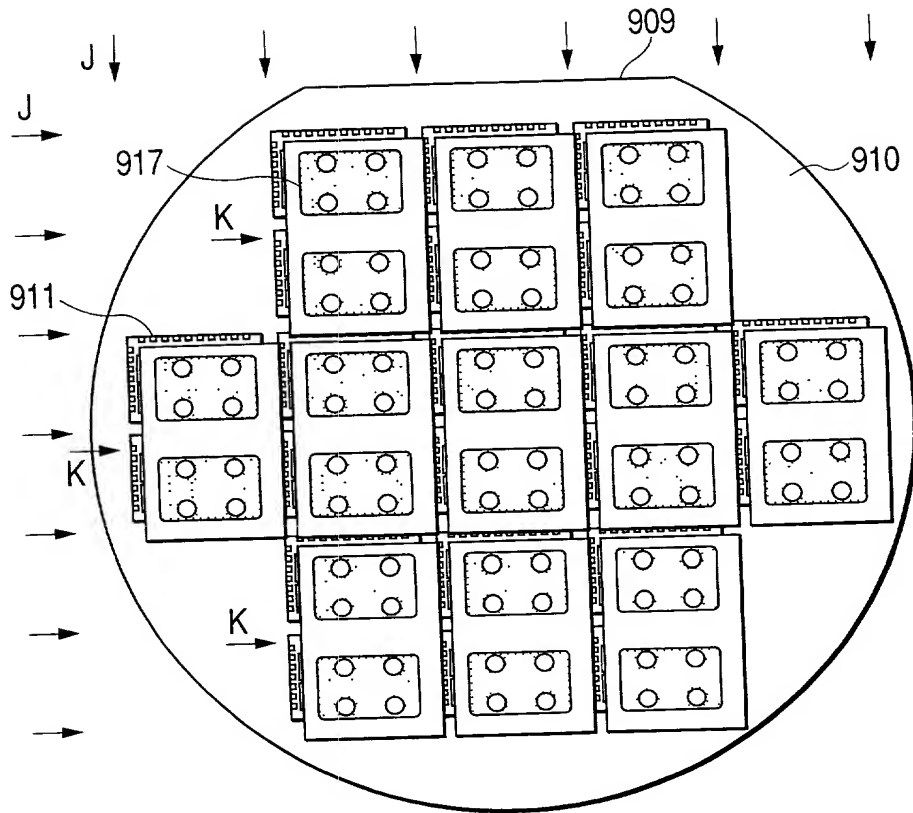
*FIG. 33*

FIG. 34



This cross-sectional view shows a semiconductor device with a substrate 517. A layer 503 is formed on the substrate. A patterned layer 509 is on top of 503. A layer 506 is on top of 509. A layer 811 is on top of 506. A layer 813 is on top of 811. A layer 512 is on top of 813. A layer 521 is on top of 512. A layer 520 is on top of 521. A layer 522 is on top of 521. A layer 601 is on top of 503. A layer 508 is on top of 601. A layer 516 is on top of 508. A layer 603 is on top of 516.



FIG. 37

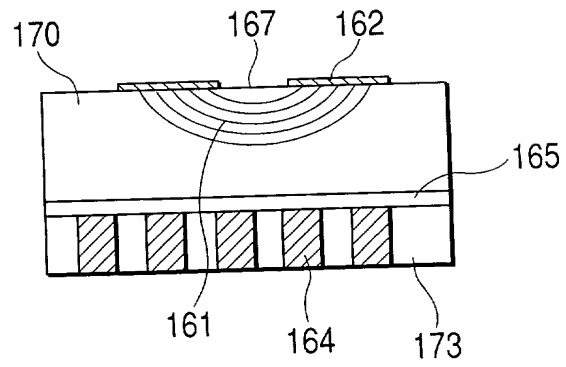


FIG. 38

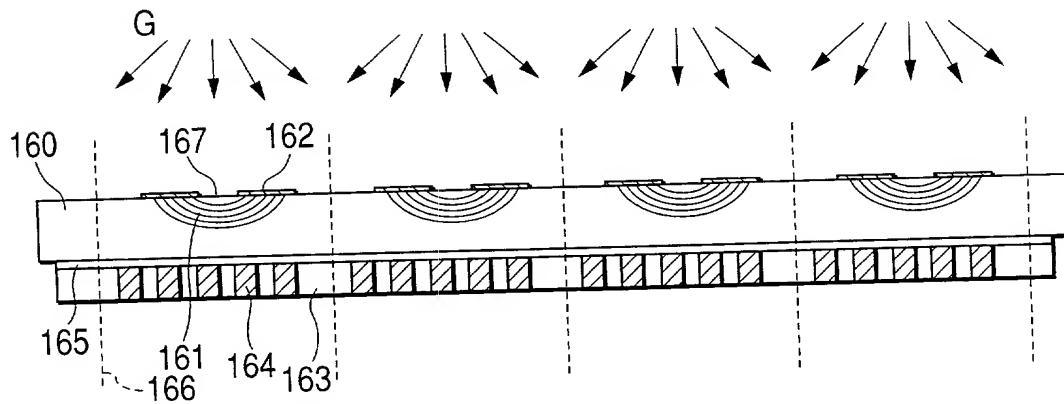


FIG. 39

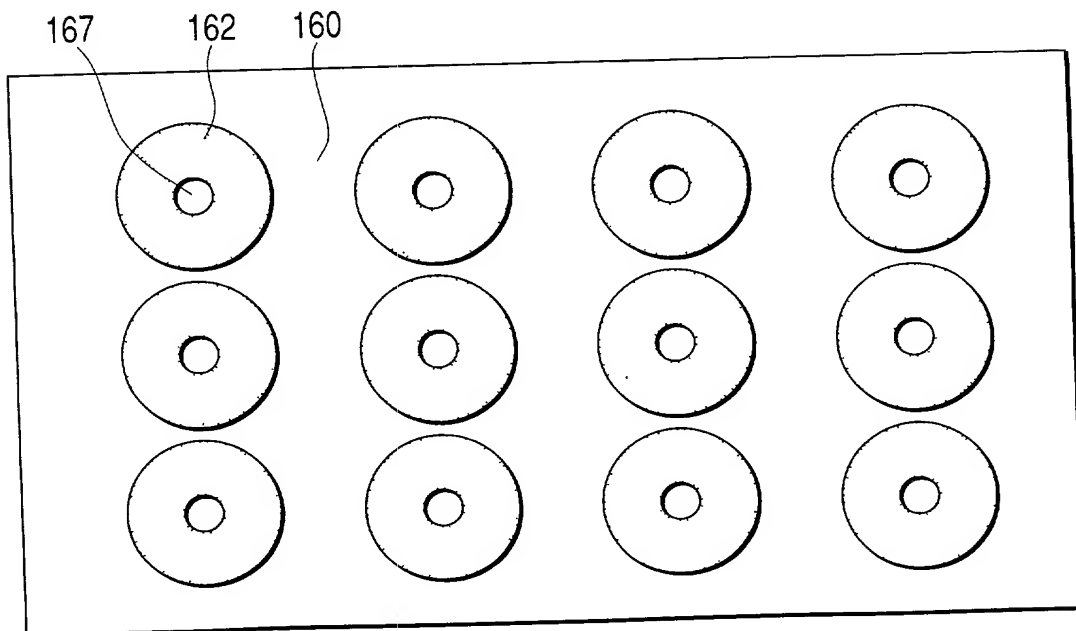


FIG. 40A

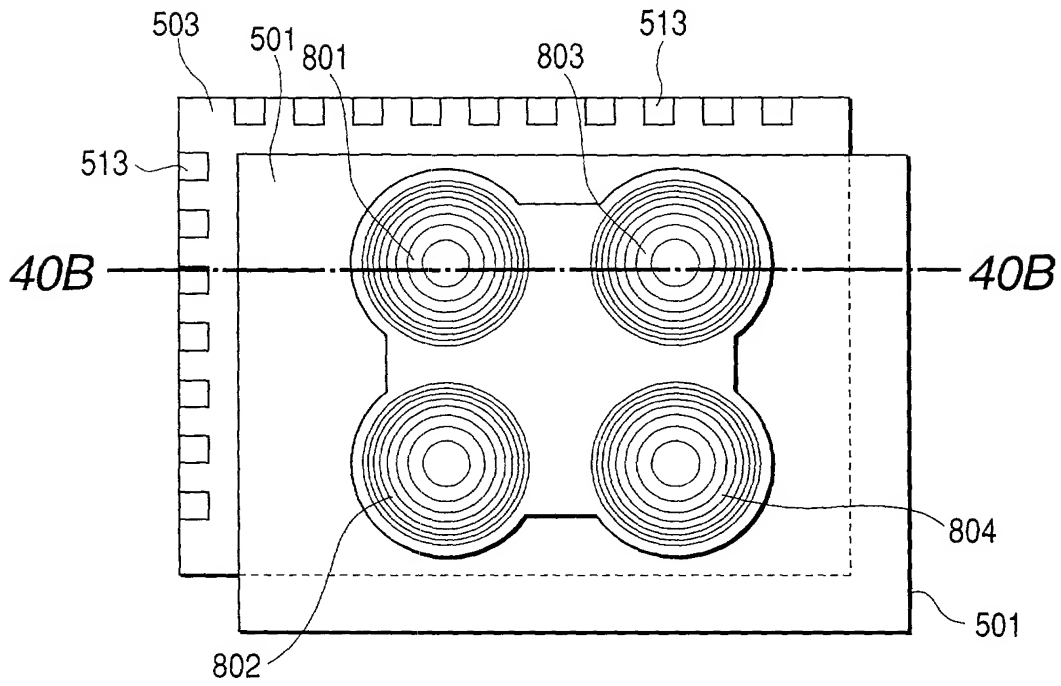


FIG. 40B

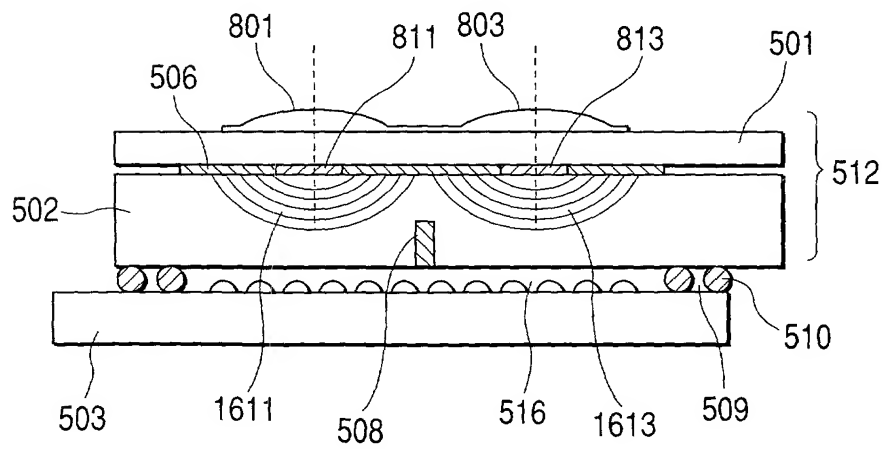


FIG. 42

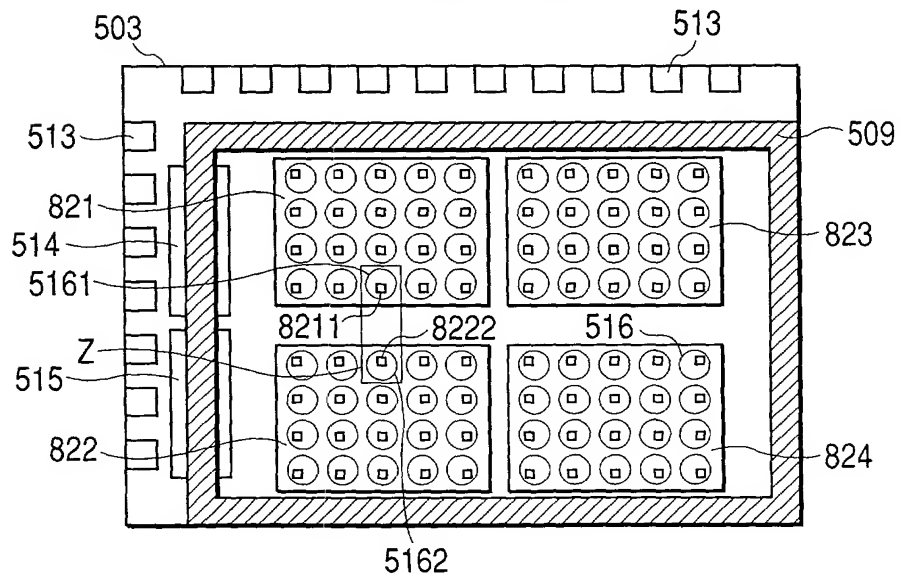


FIG. 44

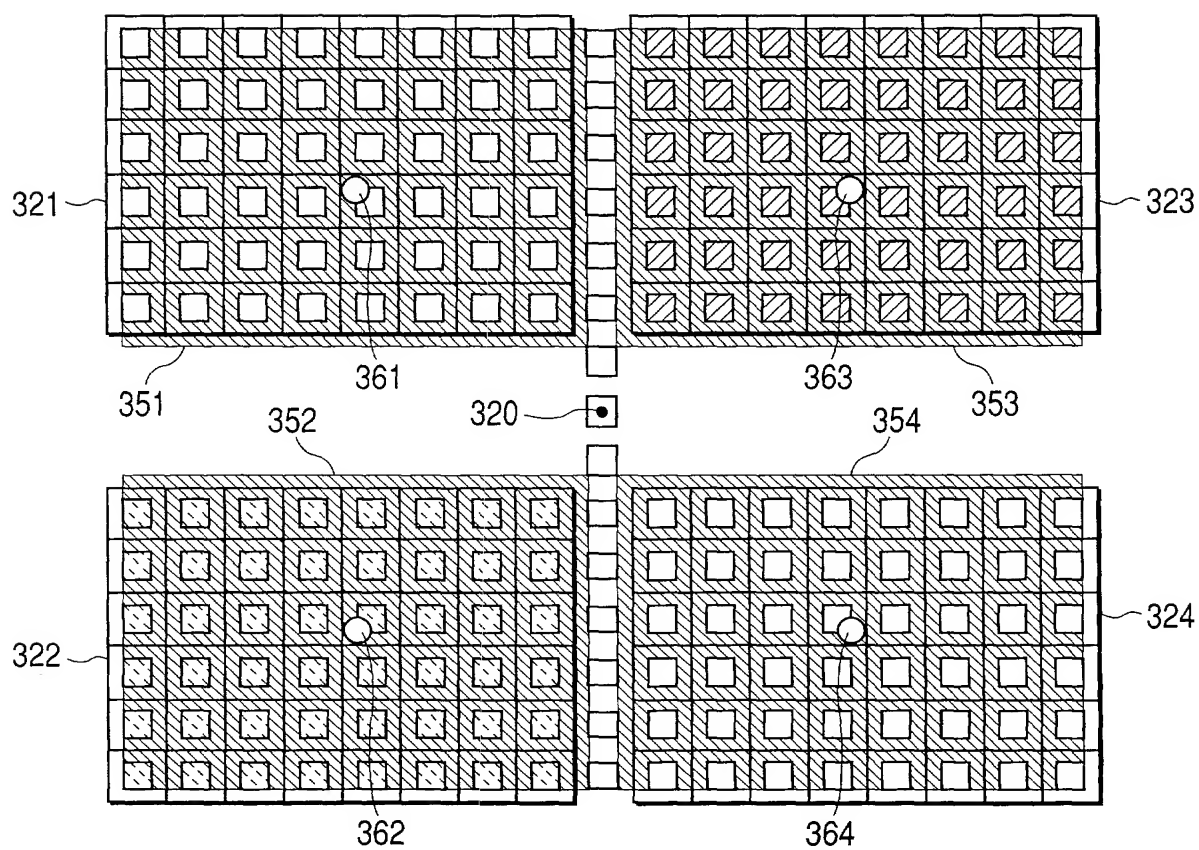


FIG. 45

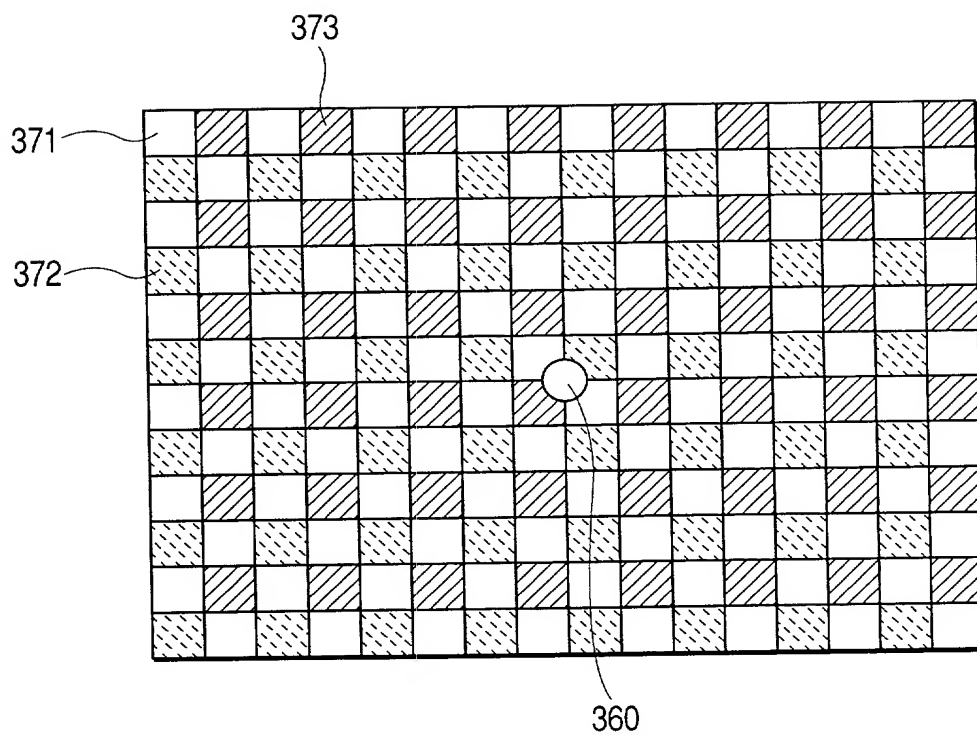


FIG. 46

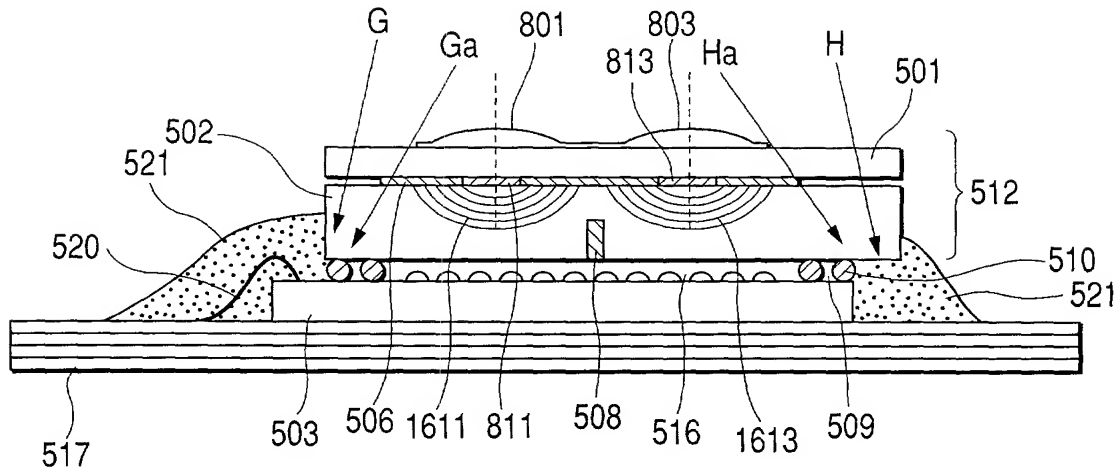


FIG. 47

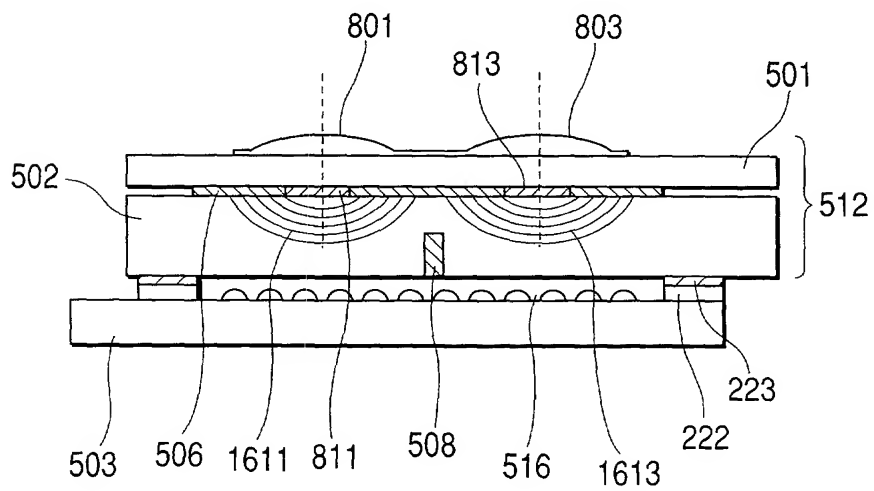


FIG. 48

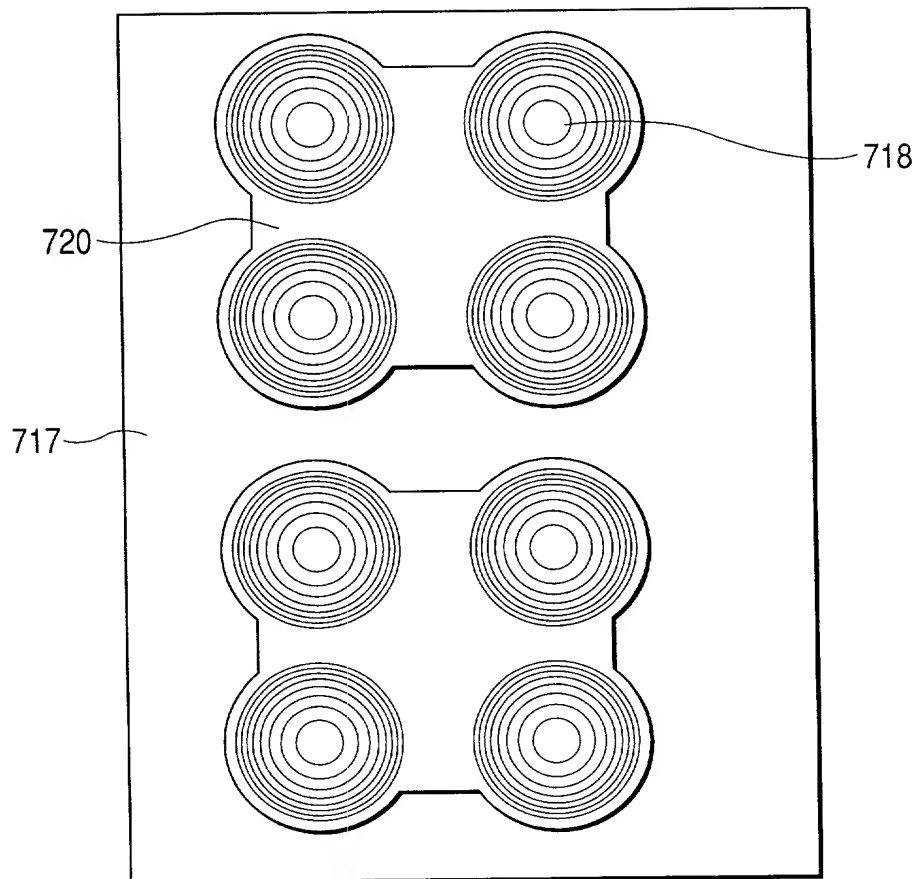
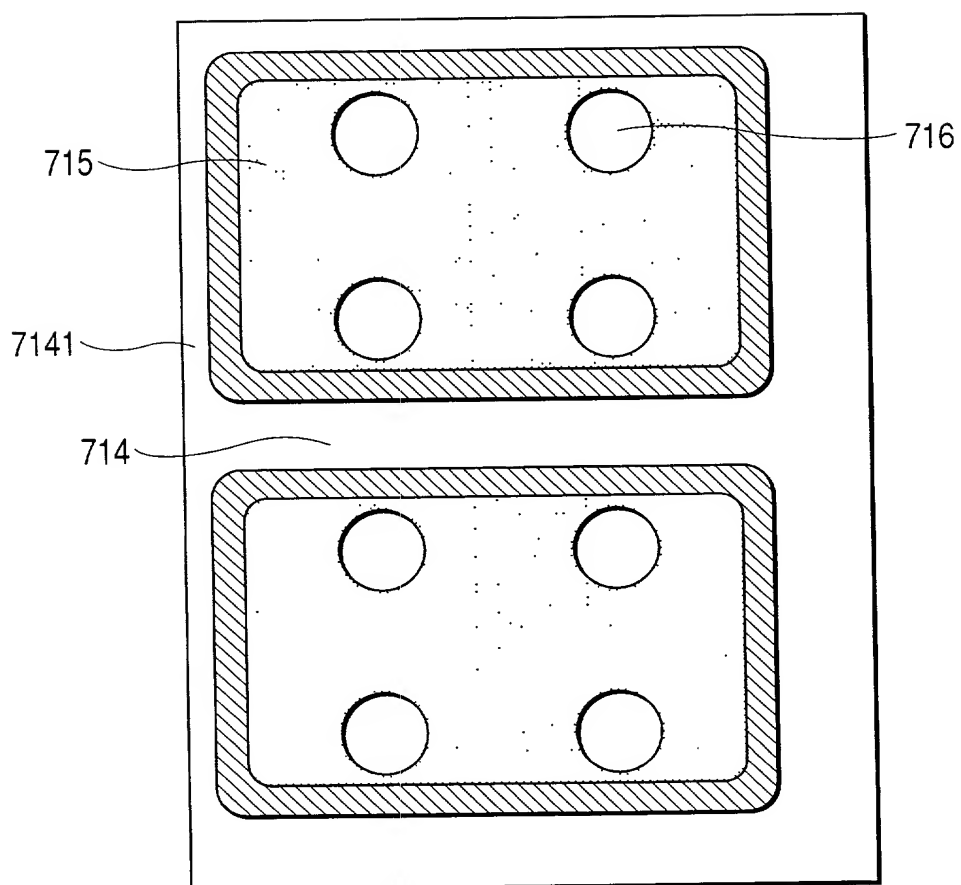
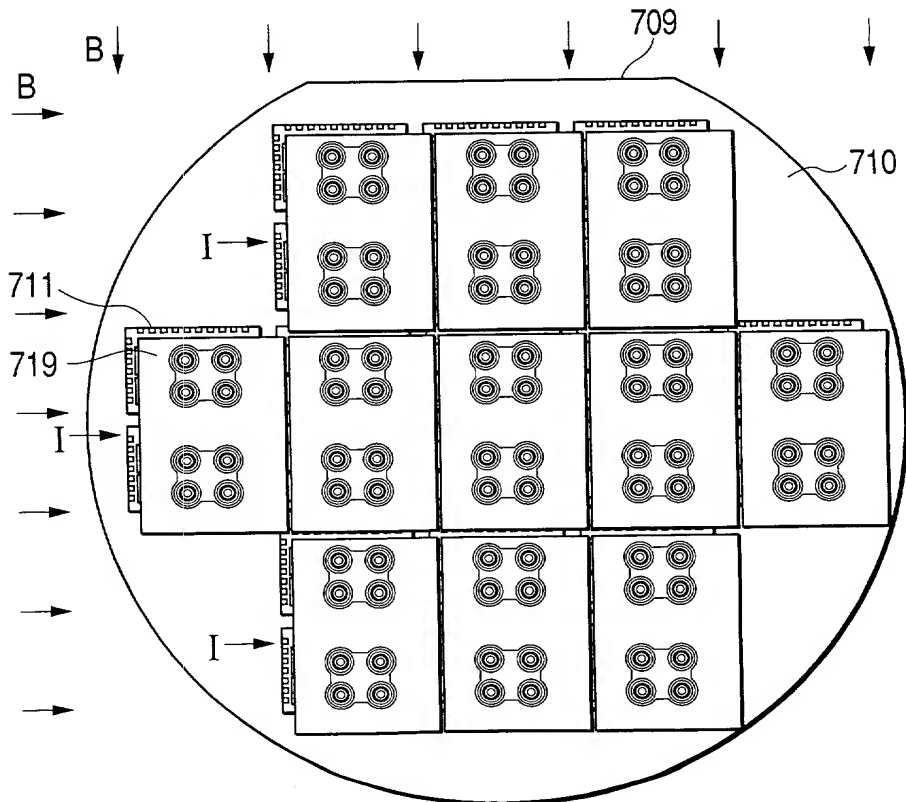


FIG. 49

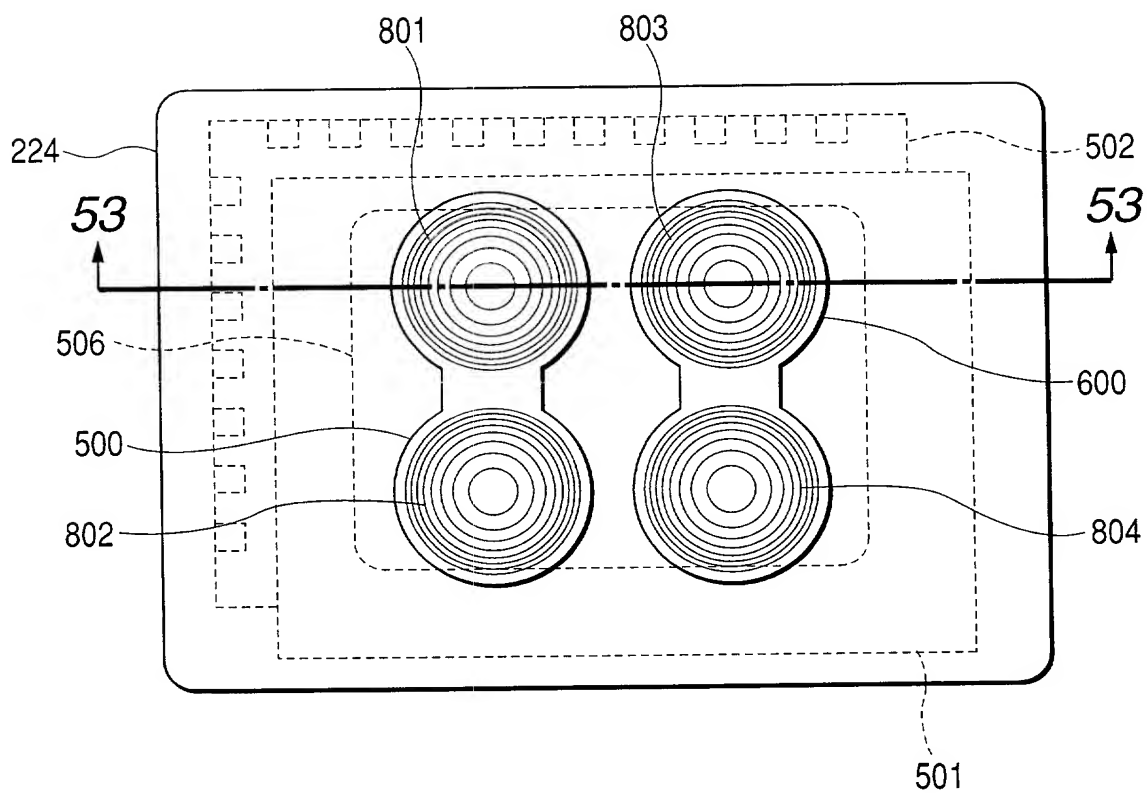




**FIG. 51**



**FIG. 52**



**FIG. 53**

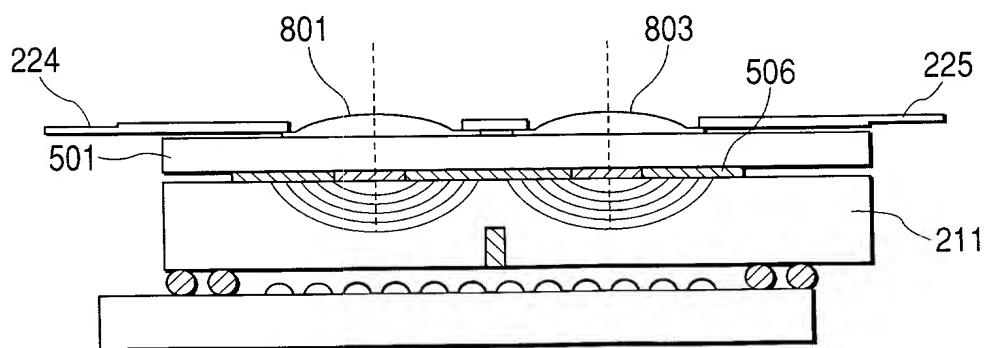


FIG. 54B      FIG. 54A      FIG. 54C

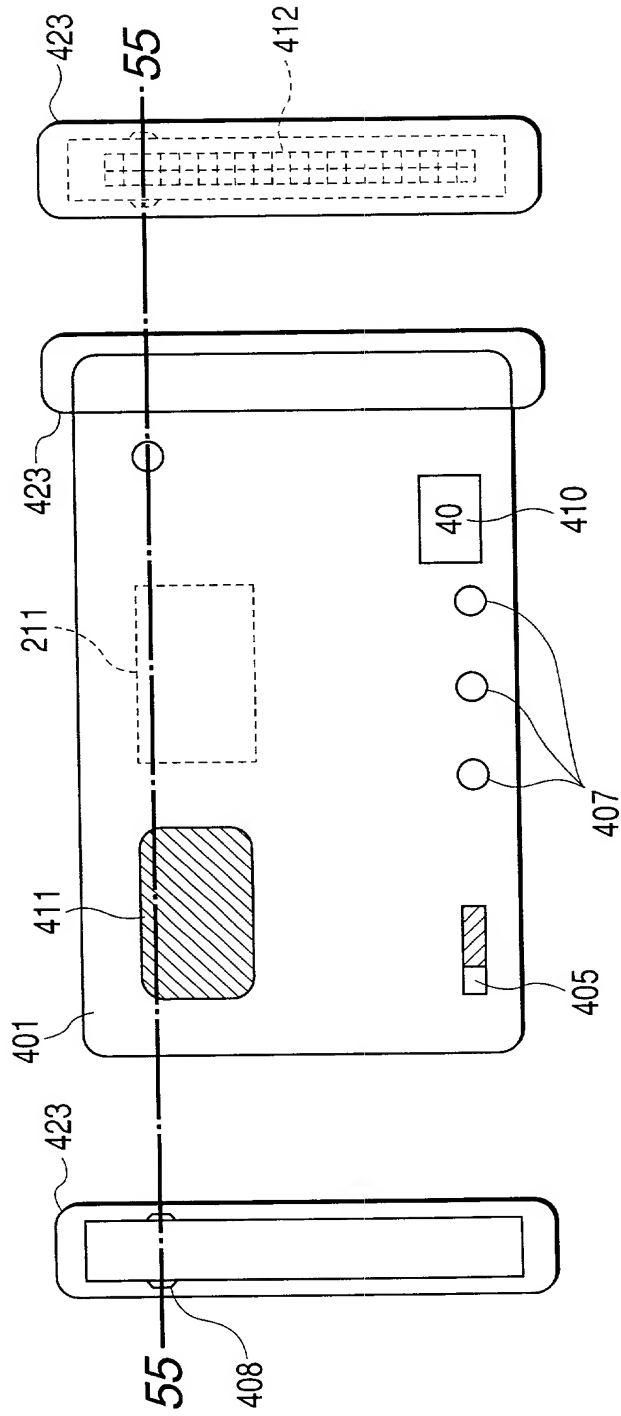


FIG. 55

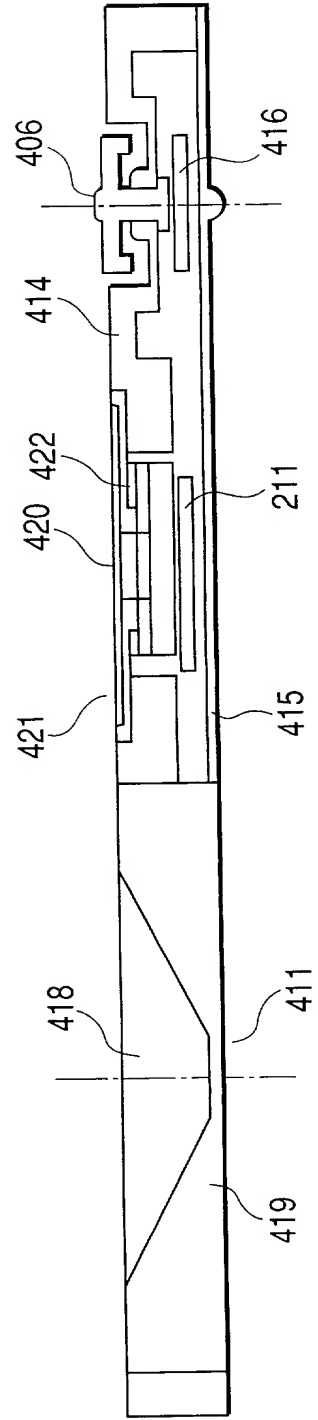
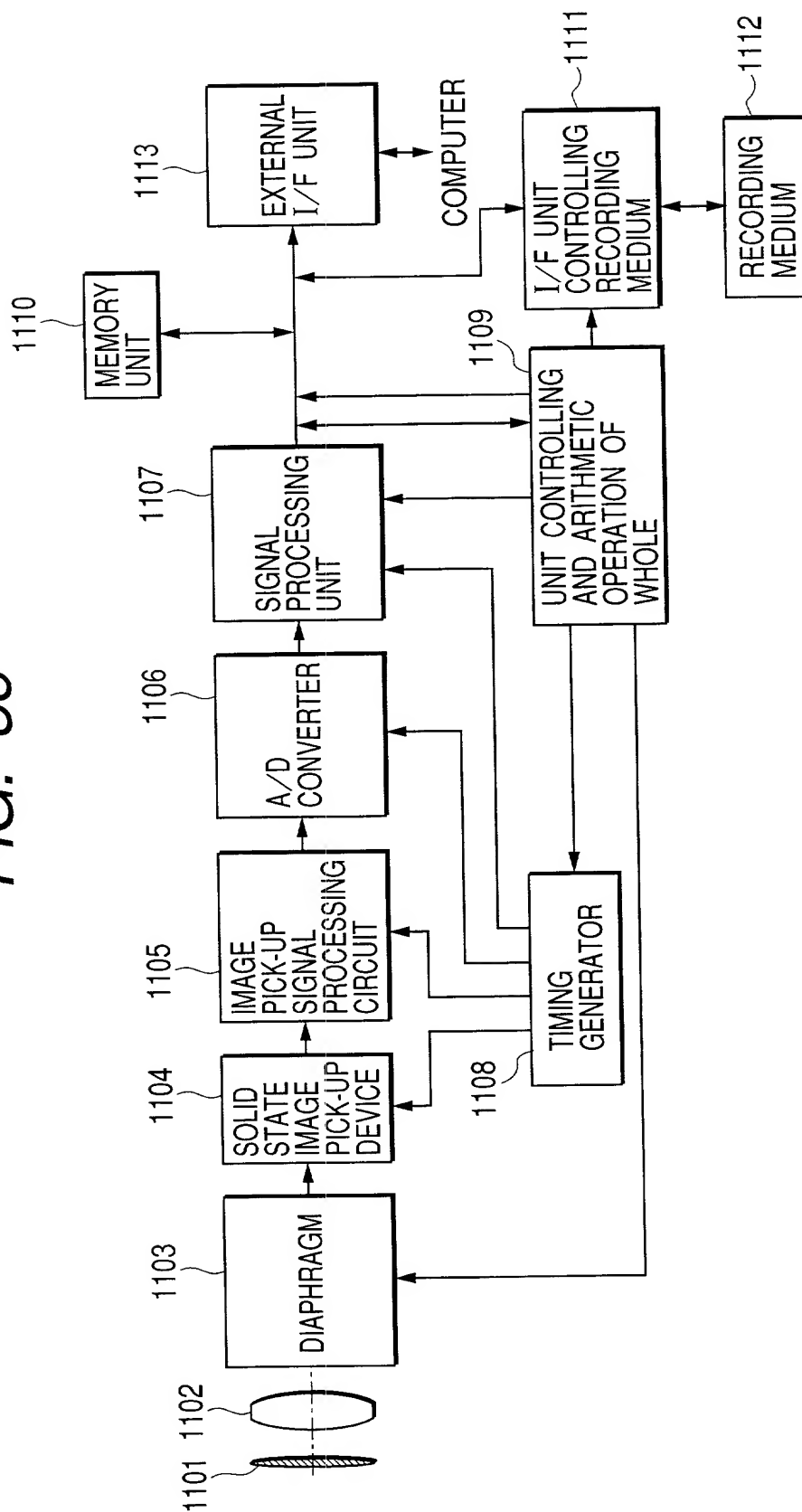
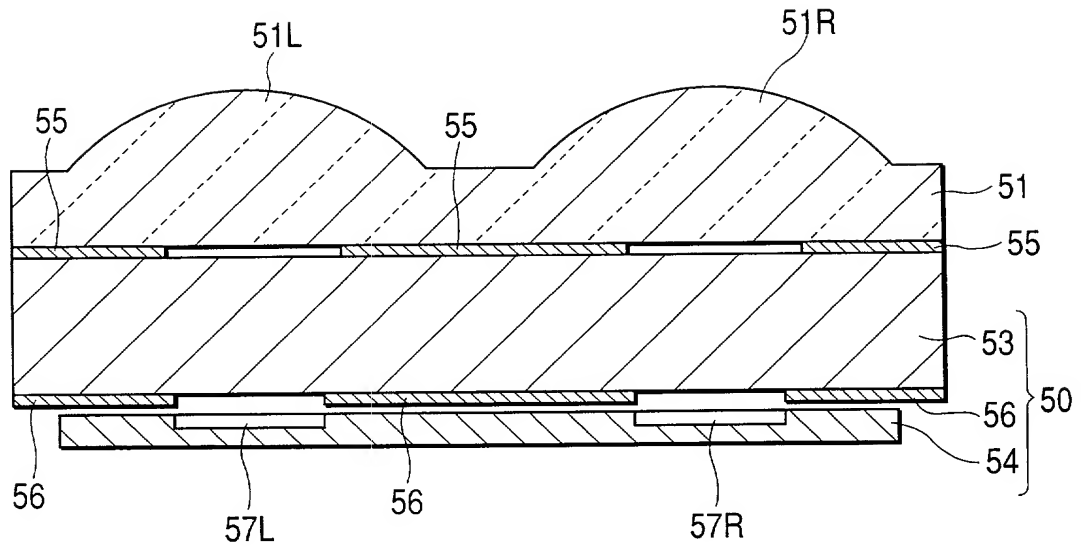


FIG. 56



*FIG. 57A*



*FIG. 57B*

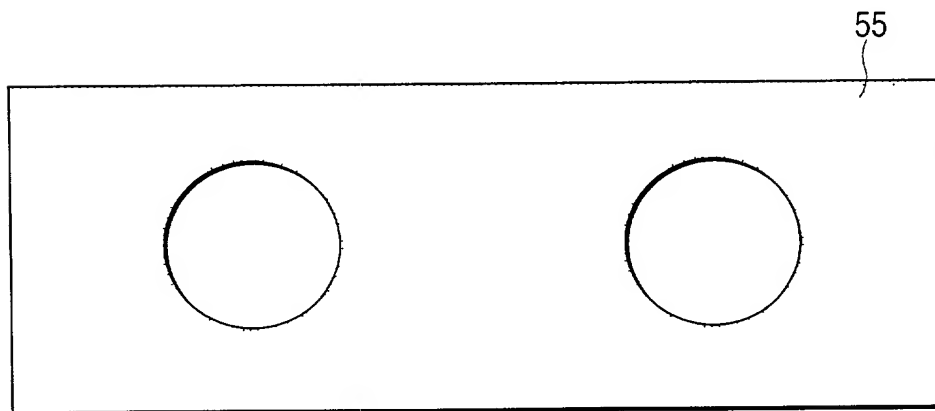


FIG. 58A

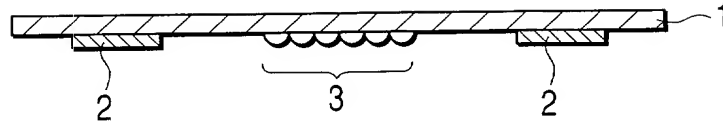


FIG. 58B

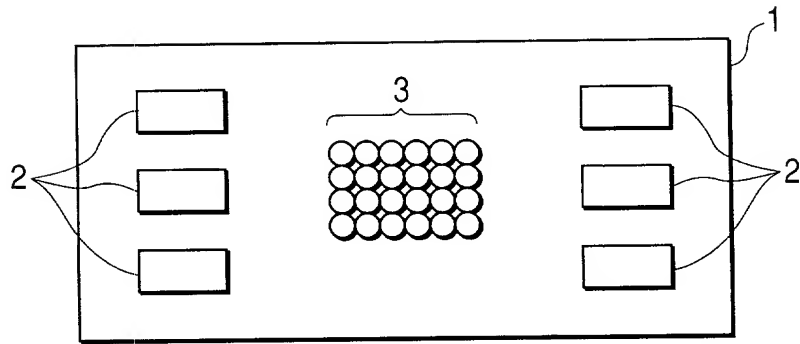


FIG. 58C

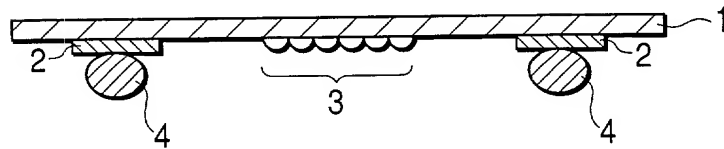


FIG. 58D

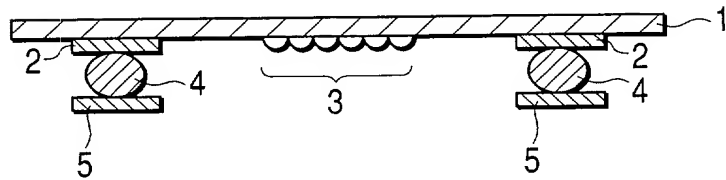


FIG. 58E

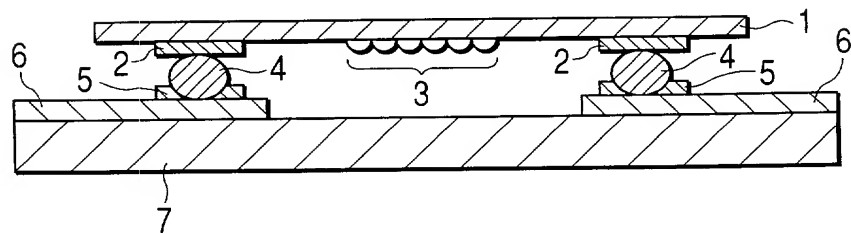


FIG. 58F

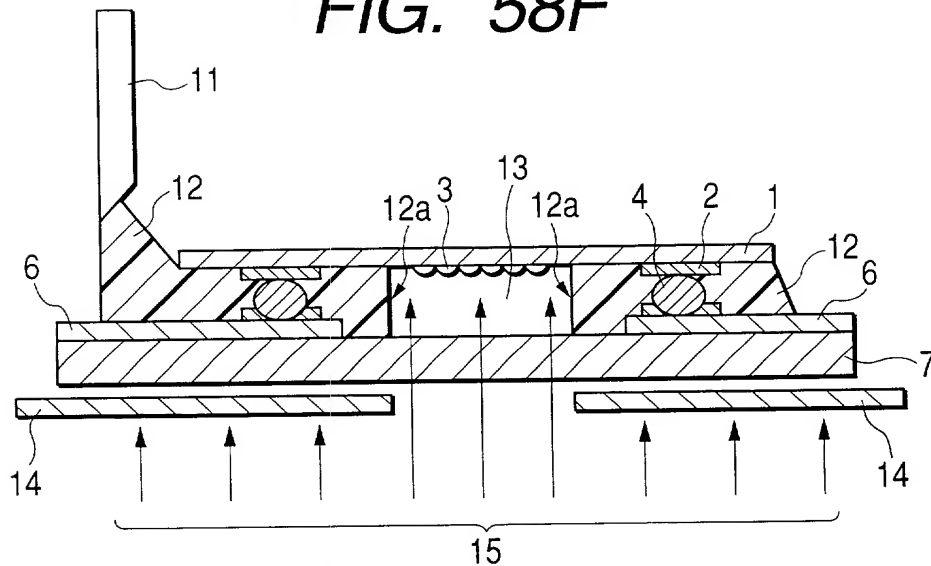


FIG. 58G

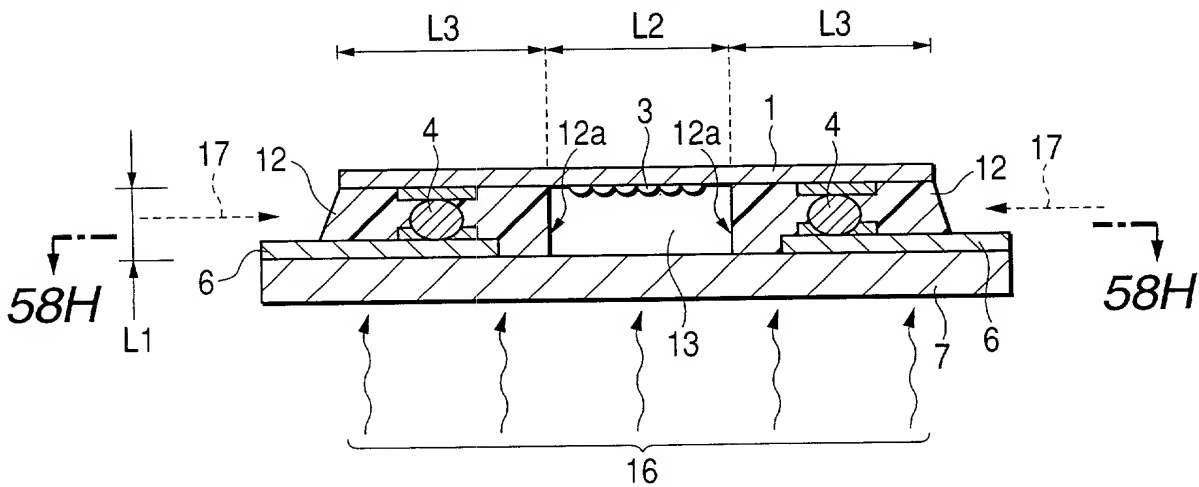


FIG. 58H

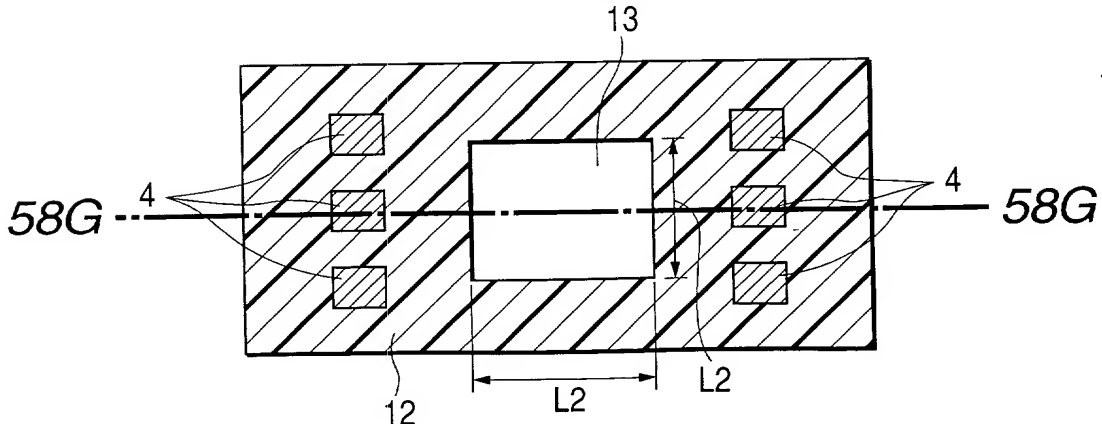


FIG. 59

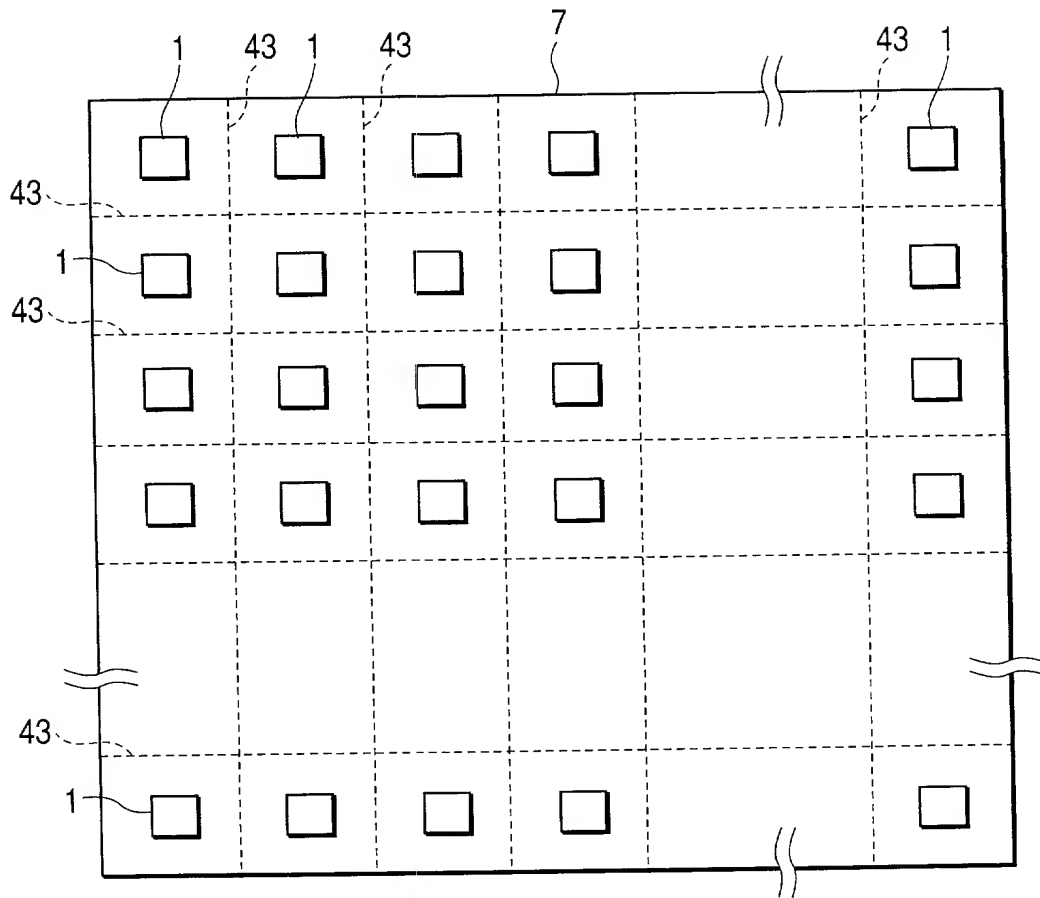




FIG. 60

